



## CHAPTER 3

# ML-Series Card Overview

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This chapter provides an overview of the ML-Series cards (ML1000-2, ML100T-12, ML100X-8) for the Cisco ONS 15454 (SONET) and Cisco ONS 15454 SDH platforms. It lists Ethernet, SONET/SDH capabilities, Cisco IOS and Cisco Transport Controller (CTC) software features, with brief descriptions of selected features.



**Note**

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For information on ML-MR-10 card, see [Chapter 27, “ML-MR-10 Card Overview.”](#)

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This chapter contains the following major sections:

- [ML-Series Card Description, page 3-1](#)
- [ML-Series Card Feature List, page 3-2](#)

## ML-Series Card Description

The ML-Series cards are independent Gigabit Ethernet (ML1000-2) or Fast Ethernet (ML100T-12 and ML100X-8) Layer 3 switches that process up to 5.7 million packets per second (Mpps). The ML-Series cards are integrated into the ONS 15454 SONET or the ONS 15454 SDH.

The Cisco IOS command-line interface (CLI) is the primary user interface for the ML-Series card. Most configuration for the card, such as Ethernet port, bridging, and VLAN, can be done only through the Cisco IOS CLI.

However, CTC, the ONS 15454 SONET/SDH graphical user interface (GUI), also supports the ML-Series card. SONET/SDH circuits cannot be provisioned through Cisco IOS, but must be configured through CTC or Transaction Language One (TL1). CTC offers ML-Series card status information, SONET/SDH alarm management, Cisco IOS Telnet session initialization, Cisco IOS configuration file management, provisioning, inventory, and other standard functions.

The ML100T-12 card features twelve RJ-45 interfaces, and the ML100X-8 and ML1000-2 cards feature two Small Form-factor Pluggable (SFP) slots supporting short wavelength (SX) and long wavelength (LX) optical modules. All three cards use the same hardware and software base and offer similar feature sets. For detailed card specifications, refer to the “Ethernet Cards” chapter of the *Cisco ONS 15454 Reference Manual* or the *Cisco ONS 15454 SDH Reference Manual*.

The ML-Series cards feature two virtual packet-over-SONET/SDH (POS) ports, which function in a manner similar to OC-N/STM-N card ports. The SONET/SDH circuits are provisioned through CTC in the same manner as standard OC-N/STM-N card circuits. The ML-Series card POS ports support virtual concatenation (VCAT) of SONET/SDH circuits and a software link capacity adjustment scheme (SW-LCAS).

# ML-Series Card Feature List

Table 3-1 provides the list of features supported on the ML-Series cards.



**Note**

For detailed information on features supported by ML-MR-10 card, see [Chapter 27, “ML-MR-10 Card Overview.”](#)

**Table 3-1** Features Supported on ML-Series cards

Feature	ML100T-12	ML100X-8	ML1000-2
<b>Layer 1 Data</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>IEEE 802.3z (Gigabit Ethernet) and IEEE 802.3x (Fast Ethernet) Flow Control</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>IEEE 802.3ad Link Aggregation Control Protocol</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>100BASE-FX full-duplex data transmission with Auto-MDIX (ML100X-8)</li> </ul>	Y	Y	N
<b>SONET/SDH</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>High-level data link control (HDLC)</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>(GFP-F) framing mechanism for POS</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>POS virtual ports</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>LEX or Point-to-Point</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Cisco HDLC</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Protocol/Bridging Control Protocol (PPP/BCP) encapsulation for POS</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>VCAT with SW-LCAS</li> </ul>	Y	Y	Y
<b>Layer 2 Feature Set</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>Transparent bridging</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>MAC address learning, aging, and switching by hardware</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Protocol tunneling</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Multiple Spanning Tree (MST) protocol tunneling</li> </ul>	N	N	N
<ul style="list-style-type: none"> <li>Integrated routing and bridging (IRB)</li> </ul>	Y	Y	Y

**Table 3-1 Features Supported on ML-Series cards**

Feature	ML100T-12	ML100X-8	ML1000-2
<ul style="list-style-type: none"> <li>IEEE 802.1Q-in-Q VLAN tunneling</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>IEEE 802.1D Spanning Tree Protocol (STP) and IEEE 802.1W Rapid Spanning Tree Protocol (RSTP)</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>IEEE 802.1D STP instance per bridge group</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Ethernet over Multiprotocol Label Switching (EoMPLS)</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>EoMPLS traffic engineering (EoMPLS-TE) with RSVP</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>VLAN-transparent and VLAN-specific services (Ethernet Relay Multipoint Service [ERMS])</li> </ul>	Y	Y	Y
<b>IEEE-RPR (802.17b)</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>Bridging as specified in the IEEE 802.17b spatially aware sublayer amendment</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Shortest path forwarding through topology discovery</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Addressing including unicast, multicast, and simple broadcast data transfers.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Bidirectional multicast frames flood around the ring using both east and west ringlets.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>The time to live (TTL) of the multicast frames is set to the equidistant span in a closed ring and the failed span in an open ring.</li> </ul>	Y	Y	Y
<b>RPR-IEEE Service Qualities</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>Per-service-quality flow-control protocols regulate traffic introduced by clients.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Class A allocated or guaranteed bandwidth has low circumference-independent jitter.</li> </ul>	Y	Y	Y

Table 3-1 Features Supported on ML-Series cards

Feature	ML100T-12	ML100X-8	ML1000-2
<ul style="list-style-type: none"> <li>Class B allocated or guaranteed bandwidth has bounded circumference-dependent jitter. This class allows for transmissions of excess information rate (EIR) bandwidths (with class C properties).</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Class C provides best-effort services.</li> </ul>	Y	Y	Y
<b>RPR-IEEE Design Strategies Increase Effective Bandwidths Beyond Those of a Broadcast Ring</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>Clockwise and counterclockwise transmissions can be concurrent.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Bandwidths can be reallocated on nonoverlapping segments.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Bandwidth reclamation. Unused bandwidths can be reclaimed by opportunistic services.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Spatial bandwidth reuse. Opportunistic bandwidths are reused on nonoverlapping segments.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Temporal bandwidth reuse. Unused opportunistic bandwidth can be consumed by others.</li> </ul>	Y	Y	Y
<b>RPR-IEEE Fairness Features Ensure Proper Partitioning of Opportunistic Traffic</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>Weighted fairness allows a weighted fair access to available ring capacity.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Aggressive fairness is supported.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Single Choke Fairness Supports generation, termination, and processing of Single Choke Fairness frames on both spans.</li> </ul>	Y	Y	Y

Table 3-1 Features Supported on ML-Series cards

Feature	ML100T-12	ML100X-8	ML1000-2
<ul style="list-style-type: none"> <li>RPR-IEEE plug-and-play automatic topology discovery and advertisement of station capabilities allow systems to become operational without manual intervention.</li> </ul>	Y	Y	Y
<b>RPR-IEEE Multiple Robust Frame Transmissions</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>Service restoration time is less than 60 milliseconds after a station or link failure.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Queue and shaper specifications avoid frame loss in normal operation.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Fully distributed control architecture eliminates single points of failure.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Operations, administration, and maintenance support service provider environments.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>EoMPLS on RPR-IEEE</li> </ul>	N	N	N
<ul style="list-style-type: none"> <li>IP forwarding on RPR-IEEE</li> </ul>	N	N	N
<ul style="list-style-type: none"> <li>Wrapping, the optional IEEE 802.17b protection scheme</li> </ul>	N	N	N
<ul style="list-style-type: none"> <li>Steering, the protection scheme</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Layer 3 control path routing</li> </ul>	Y	Y	Y
<b>Cisco Proprietary RPR</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>Ethernet frame check sequence (FCS) preservation for customers.</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Cyclic redundancy check (CRC) error alarm generation</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>FCS detection and threshold configuration</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Shortest path determination</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Keep alives</li> </ul>	Y	Y	Y
<b>EtherChannel Support</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>Bundling of ports</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Load based on MAC addresses</li> </ul>	Y	Y	Y

Table 3-1 Features Supported on ML-Series cards

Feature	ML100T-12	ML100X-8	ML1000-2
• Load Sharing based on incoming VLAN	N	N	N
• Load sharing based on Port	Y	Y	Y
• IRB	Y	Y	Y
• IEEE 802.1Q trunking	Y	Y	Y
<b>POS Channel</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
• Bundling the two POS ports	Y	Y	Y
• LEX encapsulation only	Y	Y	Y
• IRB	Y	Y	Y
• IEEE 802.1Q trunking	Y	Y	Y
<b>Layer 3 Routing, Switching, and Forwarding</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
• Default routes	Y	Y	Y
• IP unicast and multicast forwarding	Y	Y	Y
• Simple IP access control lists (ACLs) (both Layer 2 and Layer 3 forwarding path)	Y	Y	Y
• Extended IP ACLs in software (control-plane only)	Y	Y	Y
• IP and IP multicast routing and switching between Ethernet ports	Y	Y	Y
• Reverse Path Forwarding (RPF) multicast (not RPF unicast)	Y	Y	Y
• Load balancing among equal cost paths based on source and destination IP addresses	Y	Y	Y
• IRB routing mode support	Y	Y	Y
• IP host functionality	Y	Y	Y
<b>Routing Protocols</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
• Virtual Private Network (VPN) Routing and Forwarding Lite (VRF Lite)	Y	Y	Y
• Intermediate System-to-Intermediate System (IS-IS) Protocol	Y	Y	Y
• Routing Information Protocol (RIP and RIP II)	Y	Y	Y

**Table 3-1 Features Supported on ML-Series cards**

Feature	ML100T-12	ML100X-8	ML1000-2
• Enhanced Interior Gateway Routing Protocol (EIGRP)	Y	Y	Y
• Open Shortest Path First (OSPF) Protocol	Y	Y	Y
• Protocol Independent Multicast (PIM)—Sparse, sparse-dense, and dense modes	Y	Y	Y
• Secondary addressing	Y	Y	Y
• Static routes	Y	Y	Y
• Local proxy ARP	Y	Y	Y
• Border Gateway Protocol (BGP)	Y	Y	Y
• Classless interdomain routing (CIDR)	Y	Y	Y
<b>Quality of Service (QoS)</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
• Multicast priority queuing classes	Y	Y	Y
• Service level agreements (SLAs) with 1-Mbps granularity	Y	Y	Y
• Input policing	Y	Y	Y
• Guaranteed bandwidth (weighted round-robin [WDRR] plus strict priority scheduling)	Y	Y	Y
• Low latency queuing support for unicast Voice-over-IP (VoIP)	Y	Y	Y
• Class of service (CoS) based on Layer 2 priority, VLAN ID, Layer 3 Type of Service/DiffServ Code Point (TOS/DSCP), and port	Y	Y	Y
• CoS-based packet statistics	Y	Y	Y
<b>Metro Ethernet Feature Set: Ethernet Virtual Circuits</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
• Point-to-Point topology (UNI to UNI)	N	N	N
• Attribute Discovery Frames (ATD) for VLAN mapping	N	N	N

Table 3-1 Features Supported on ML-Series cards

Feature	ML100T-12	ML100X-8	ML1000-2
<b>Security Features</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
• Cisco IOS login enhancements	Y	Y	Y
• Secure Shell connection (SSH Version 2)	Y	Y	Y
• Disabled console port	Y	Y	Y
• Authentication, Authorization, and Accounting/Remote Authentication Dial-In User Service (AAA/RADIUS) stand alone mode	Y	Y	Y
• AAA/RADIUS relay mode	Y	Y	Y
<b>Additional Protocols</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
• Cisco Discovery Protocol (CDP) support on Ethernet ports	Y	Y	Y
• Dynamic Host Configuration Protocol (DHCP) relay	N	N	N
• Hot Standby Router Protocol (HSRP) over 10/100 Ethernet, Gigabit Ethernet, FEC, GEC, and Bridge Group Virtual Interface (BVI)	Y	Y	Y
• Internet Control Message Protocol (ICMP)	Y	Y	Y
<b>Management Features</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
• Cisco IOS	Y	Y	Y
• CTC	Y	Y	Y
• CTM	Y	Y	Y
• Remote monitoring (RMON)	Y	Y	Y
• Simple Network Management Protocol (SNMP)	Y	Y	Y
• Transaction Language 1 (TL1)	Y	Y	Y
• Simultaneous performance monitoring (PM) counter clearing in Cisco IOS, CTC, and TL1	Y	Y	Y



**Table 3-1 Features Supported on ML-Series cards**

Feature	ML100T-12	ML100X-8	ML1000-2
<b>System Features</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>Automatic field programmable gate array (FPGA) Upgrade</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Network Equipment Building Systems 3 (NEBS3) compliant</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Version up to independently upgrade individual cards</li> </ul>	Y	Y	Y
<b>CTC Features</b>	Y (R 6.0 and above)	Y (R 6.0 and above)	Y (R 6.0 and above)
<ul style="list-style-type: none"> <li>Framing Mode Provisioning</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Standard STS/STM and VCAT circuit provisioning for POS virtual ports</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>SONET/SDH alarm reporting for path alarms and other ML-Series card specific alarms</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Raw port statistics</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Standard inventory and card management functions</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>J1 path trace</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Cisco IOS CLI Telnet sessions from CTC</li> </ul>	Y	Y	Y
<ul style="list-style-type: none"> <li>Cisco IOS startup configuration file management from CTC</li> </ul>	Y	Y	Y

