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In your requests please include the following reference number 78EE117C99-1070853524

Contents

1.1 rabbitmq-server 3.1.5

1.1.1 Available under license

1.2 python 2.7.8

1.2.1 Available under license

1.3 django 1.5.1

1.3.1 Available under license

1.4 pycairo 1.8.6 2.1.el6

1.4.1 Available under license

1.5 riemann 0.2.1

1.5.1 Available under license

1.6 ruby 2.2.4

1.6.1 Available under license

1.7 curl 7.44.0

1.7.1 Available under license

1.8 ciscosafec 3.0.0

1.8.1 Available under license

1.9 geo-ip 1.6.9 1

1.9.1 Available under license

1.10 jruby-complete 1.7.9

1.10.1 Available under license

1.11 tmux 2.5 3build1

1.11.1 Available under license

1.12 libmemcached 1.0.18 4.2

1.12.1 Available under license

1.13 libuuid 2.21.2

1.13.1 Available under license

- 1.14 libxml 2.9.1 6.1.el7.centos.3**
 - 1.14.1 Available under license
- 1.15 xar 1.6.1**
 - 1.15.1 Available under license
- 1.16 proxy-connect 0.0.1**
 - 1.16.1 Available under license
- 1.17 libaio 0.3.110-1**
 - 1.17.1 Available under license
- 1.18 ruby 1.9.3.484 6.pe**
 - 1.18.1 Available under license
- 1.19 percona-xtrabackup 2.4.2**
 - 1.19.1 Available under license
- 1.20 chef 11.8.2**
 - 1.20.1 Available under license
- 1.21 ucspi-tcp 0.88**
 - 1.21.1 Available under license
- 1.22 twisted 12.2.0**
 - 1.22.1 Available under license
- 1.23 memcached 1.4.25**
 - 1.23.1 Available under license
- 1.24 utf-bom-utils 1.0**
 - 1.24.1 Available under license
- 1.25 jansson 2.7 1+deb8u1**
 - 1.25.1 Available under license
- 1.26 xmlsec 1.2.20 7.el7_4**
 - 1.26.1 Available under license
- 1.27 graphite-whisper 0.9.12**
 - 1.27.1 Available under license
- 1.28 python-setuptools 0.6c11**
 - 1.28.1 Available under license
- 1.29 ruby193-ruby 1.9.3.484 50.el6.centos.alt**
 - 1.29.1 Available under license
- 1.30 libssh 1.4.3**
- 1.31 fcgi 2.4.0 f71edf71bd1fa7ee2337a4416df0d06c**
 - 1.31.1 Available under license
- 1.32 gunicorn 0.17.4**
 - 1.32.1 Available under license
- 1.33 libxslt 1.1.28 5.el7**
 - 1.33.1 Available under license

- 1.34 glib 2.34.3**
 - 1.34.1 Available under license
- 1.35 free-type 2.4.11**
- 1.36 pixman 0.32.6**
 - 1.36.1 Available under license
- 1.37 libiconv 1.14**
 - 1.37.1 Available under license
- 1.38 jpeg 9a**
 - 1.38.1 Notifications
 - 1.38.2 Available under license
- 1.39 daemon-tools 0.76**
 - 1.39.1 Available under license
- 1.40 nginx 1.4.7**
 - 1.40.1 Available under license
- 1.41 gmp 5.0.5 2012-05-06 Torbjorn Granlund**
 - 1.41.1 Available under license
- 1.42 libevent 2.0.21**
 - 1.42.1 Available under license
- 1.43 erlang R15B03**
 - 1.43.1 Available under license
- 1.44 graphene f1910b5cdf2969ef97e88125baa7525748eef5d2**
- 1.45 tokumx 2.0.1**
 - 1.45.1 Available under license
- 1.46 zope-interface 4.0.5**
 - 1.46.1 Available under license
- 1.47 django-tagging 0.3.1 3.e16**
 - 1.47.1 Available under license
- 1.48 fontconfig 2.10.95 11.e17**
 - 1.48.1 Available under license
- 1.49 asn1c 0.9.24**
 - 1.49.1 Available under license
- 1.50 bundler 1.6.4**
 - 1.50.1 Available under license
- 1.51 bundler 1.11.2**
 - 1.51.1 Available under license
- 1.52 perl 5.20.0**
 - 1.52.1 Available under license
- 1.53 gir1.2-gdkpixbuf-2.0 2.27.0**
 - 1.53.1 Available under license

1.54 graphite-web 0.9.12 5.el6

1.54.1 Available under license

1.55 libpng 1.5.22

1.55.1 Available under license

1.56 haproxy 1.7.2

1.56.1 Available under license

1.57 graphite-carbon 0.9.12

1.57.1 Available under license

1.58 stunnel 4.56 6.el7

1.58.1 Available under license

1.59 log4cxx 0.10.0 13ubuntu2

1.59.1 Available under license

1.60 libcroco 0.6.8 5.el7

1.60.1 Available under license

1.61 percona-server 5.6.15 63.0

1.61.1 Available under license

1.62 libbsd 0.4.2 1

1.62.1 Available under license

1.63 bar 1.11.1

1.63.1 Available under license

1.64 urw-fonts 1.0.7pre41

1.64.1 Available under license

1.65 wkhtmltopdf 0.12.2.1

1.65.1 Available under license

1.66 openssl 1.0.2j

1.66.1 Notifications

1.66.2 Available under license

1.1 rabbitmq-server 3.1.5

1.1.1 Available under license :

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1.2 python 2.7.8

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History of the software

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl/>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us/>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation; see <http://www.zope.com/>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	no
2.1	2.0+1.6.1	2001	PSF	no
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2.1.1	2.1+2.0.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2 and above	2.1.1	2001-now	PSF	yes

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Mersenne Twister

The `:mod:`_random`` module includes code based on a download from <http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/MT2002/emt19937ar.html>. The following are the verbatim comments from the original code::

A C-program for MT19937, with initialization improved 2002/1/26.

Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using `init_genrand(seed)`
or `init_by_array(init_key, key_length)`.

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Sockets

The `:mod:`socket`` module uses the functions, `:func:`getaddrinfo``, and
`:func:`getnameinfo``, which are coded in separate source files from the WIDE
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MD5 message digest algorithm

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L. Peter Deutsch
ghost@aladdin.com

Independent implementation of MD5 (RFC 1321).

This code implements the MD5 Algorithm defined in RFC 1321, whose text is available at

<http://www.ietf.org/rfc/rfc1321.txt>

The code is derived from the text of the RFC, including the test suite (section A.5) but excluding the rest of Appendix A. It does not include any code or documentation that is identified in the RFC as being copyrighted.

The original and principal author of md5.h is L. Peter Deutsch <ghost@aladdin.com>. Other authors are noted in the change history that follows (in reverse chronological order):

2002-04-13 lpd Removed support for non-ANSI compilers; removed references to Ghostscript; clarified derivation from RFC 1321; now handles byte order either statically or dynamically.
1999-11-04 lpd Edited comments slightly for automatic TOC extraction.
1999-10-18 lpd Fixed typo in header comment (ansi2knr rather than md5); added conditionalization for C++ compilation from Martin Purschke <purschke@bnl.gov>.
1999-05-03 lpd Original version.

Asynchronous socket services

The :mod:`asynchat` and :mod:`asyncore` modules contain the following notice::

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Modified by Jack Jansen, CWI, July 1995:

- Use `binascii` module to do the actual line-by-line conversion between `ascii` and `binary`. This results in a 1000-fold speedup. The C version is still 5 times faster, though.
- Arguments more compliant with Python standard

XML Remote Procedure Calls

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strtod and dtoa

The file `:file:`Python/dtoa.c``, which supplies C functions `dtoa` and `strtod` for conversion of C doubles to and from strings, is derived from the file of the same name by David M. Gay, currently available from <http://www.netlib.org/fp/>. The original file, as retrieved on March 16, 2009, contains the following copyright and licensing notice::

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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

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1.3 django 1.5.1

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Advice for new contributors

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New contributor and not sure what to do? Want to help but just don't know how to get started? This is the section for you.

First steps

Start with these easy tasks to discover Django's development process.

****Triage tickets****

If an `unreviewed ticket` reports a bug, try and reproduce it. If you can reproduce it and it seems valid, make a note that you confirmed the bug and accept the ticket. Make sure the ticket is filed under the correct component area. Consider writing a patch that adds a test for the bug's behavior, even if you don't fix the bug itself. See more at

:ref:`how-can-i-help-with-triaging`

****Look for tickets that are accepted and review patches to build familiarity with the codebase and the process****

Mark the appropriate flags if a patch needs docs or tests. Look through the changes a patch makes, and keep an eye out for syntax that is incompatible with older but still supported versions of Python. Run the tests and make sure they pass on your system. Where possible and relevant, try them out on a database other than SQLite. Leave comments and feedback!

****Keep old patches up to date****

Oftentimes the codebase will change between a patch being submitted and the time it gets reviewed. Make sure it still applies cleanly and functions as expected. Simply updating a patch is both useful and important! See more on :doc:`writing-code/submitting-patches`.

****Write some documentation****

Django's documentation is great but it can always be improved. Did you find a typo? Do you think that something should be clarified? Go ahead and suggest a documentation patch! See also the guide on :doc:`writing-documentation`, in particular the tips for :ref:`improving-the-documentation`.

.. note::

The `reports page`_ contains links to many useful Trac queries, including several that are useful for triaging tickets and reviewing patches as suggested above.

.. _reports page: <https://code.djangoproject.com/wiki/Reports>

.. _unreviewed ticket: <https://code.djangoproject.com/query?status=!closed&stage=Unreviewed>

Guidelines

As a newcomer on a large project, it's easy to experience frustration. Here's some advice to make your work on Django more useful and rewarding.

****Pick a subject area that you care about, that you are familiar with, or that you want to learn about****

You don't already have to be an expert on the area you want to work on; you become an expert through your ongoing contributions to the code.

****Analyze tickets' context and history****

Trac isn't an absolute; the context is just as important as the words.

When reading Trac, you need to take into account who says things, and when they were said. Support for an idea two years ago doesn't necessarily mean that the idea will still have support. You also need to pay attention to who *hasn't* spoken -- for example, if a core team member hasn't been recently involved in a discussion, then a ticket may not have the support required to get into trunk.

****Start small****

It's easier to get feedback on a little issue than on a big one. See the ``easy pickings`_`.

****If you're going to engage in a big task, make sure that your idea has support first****

This means getting someone else to confirm that a bug is real before you fix the issue, and ensuring that the core team supports a proposed feature before you go implementing it.

****Be bold! Leave feedback!****

Sometimes it can be scary to put your opinion out to the world and say "this ticket is correct" or "this patch needs work", but it's the only way the project moves forward. The contributions of the broad Django community ultimately have a much greater impact than that of the core developers. We can't do it without YOU!

****Err on the side of caution when marking things Ready For Check-in****

If you're really not certain if a ticket is ready, don't mark it as such. Leave a comment instead, letting others know your thoughts. If you're mostly certain, but not completely certain, you might also try asking on IRC to see if someone else can confirm your suspicions.

****Wait for feedback, and respond to feedback that you receive****

Focus on one or two tickets, see them through from start to finish, and repeat. The shotgun approach of taking on lots of tickets and letting some fall by the wayside ends up doing more harm than good.

****Be rigorous****

When we say "pep:8", and must have docs and tests", we mean it. If a patch doesn't have docs and tests, there had better be a good reason. Arguments

like "I couldn't find any existing tests of this feature" don't carry much weight--while it may be true, that means you have the extra-important job of writing the very first tests for that feature, not that you get a pass from writing tests altogether.

.. _easy pickings: <https://code.djangoproject.com/query?status=!closed&easy=1>

.. _new-contributors-faq:

FAQ

1. ****This ticket I care about has been ignored for days/weeks/months! What can I do to get it committed?***

First off, it's not personal. Django is entirely developed by volunteers (even the core developers), and sometimes folks just don't have time. The best thing to do is to send a gentle reminder to the django-developers mailing list asking for review on the ticket, or to bring it up in the #django-dev IRC channel.

2. ****I'm sure my ticket is absolutely 100% perfect, can I mark it as RFC myself?***

Short answer: No. It's always better to get another set of eyes on a ticket. If you're having trouble getting that second set of eyes, see question 1, above.

3. ****My ticket has been in DDN forever! What should I do?***

Design Decision Needed requires consensus about the right solution. At the very least it needs consensus among the core developers, and ideally it has consensus from the community as well. The best way to accomplish this is to start a thread on the django-developers mailing list, and for very complex issues to start a wiki page summarizing the problem and the possible solutions.

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1.4 pycairo 1.8.6 2.1.el6

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1.6 ruby 2.2.4

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1.7 curl 7.44.0

1.7.1 Available under license :

License Mixing with apps, libcurl and Third Party Libraries

=====

libcurl can be built to use a fair amount of various third party libraries, libraries that are written and provided by other parties that are distributed using their own licenses. Even libcurl itself contains code that may cause problems to some. This document attempts to describe what licenses libcurl and the other libraries use and what possible dilemmas linking and mixing them all can lead to for end users.

I am not a lawyer and this is not legal advice!

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BoringSSL <https://boringssl.googlesource.com/>

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libressl <http://www.libressl.org/>

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c-ares <http://daniel.haxx.se/projects/c-ares/license.html>

(Used for asynchronous name resolves) Uses an MIT license that is very liberal and imposes no restrictions on any other library or part you may link with.

zlib http://www.zlib.net/zlib_license.html

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MIT Kerberos <http://web.mit.edu/kerberos/www/dist/>

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libidn <http://josefsson.org/libidn/>

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1.8 ciscosafec 3.0.0

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1.9 geo-ip 1.6.9 1

1.9.1 Available under license :

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Version 2.1, February 1999

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1.10 jruby-complete 1.7.9

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1.11 tmux 2.5 3build1

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Upstream-Source: <http://sf.net/projects/tmux>

Upstream Author: Nicholas Marriott <nicm@users.sf.net>

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cmd-if-shell.c

cmd-load-buffer.c

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1.12 libmemcached 1.0.18 4.2

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Upstream-Name: libMemcached

Upstream-Contact: Brian Aker <brian@tangent.org>, <https://launchpad.net/~data-differential-engineering>

Source: <https://launchpad.net/libmemcached>

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<http://burtleburtle.net/bob/hash/index.html>

Files: clients/memaslap.c clients/ms_conn.* clients/ms_memslap.h clients/ms_setting.* clients/ms_sigsegv.h clients/ms_stats.* clients/ms_task.* clients/ms_thread.*

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1.13 libuuid 2.21.2

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```
bsd.lo: bsd.c ../../config.h \
```

```
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/stdio.h \
```

```
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/features.h \
```

```
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/cdefs.h \
```

```
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/wordsize.h \
```

```
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/gnu/stubs.h \
```

```
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/include/stddef.h \
```

```
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/types.h \
```

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/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/typesizes.h \
```

```
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/libio.h \
```



```
../../../../include/bitops.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/param.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/linux/param.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm/param.h \  
../../../../include/blkdev.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/ioctl.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/ioctls.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm/ioctls.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm-generic/ioctls.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/linux/ioctl.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm/ioctl.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm-generic/ioctl.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/ioctl-types.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/ttydefaults.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/unistd.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/posix_opt.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/environments.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/confname.h \  
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/getopt.h \  
../../../../libblkid/src/blkid.h ../../include/list.h blkid_parttypes.h
```

```
../../../../config.h:
```

```
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/stdio.h:
```

```
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/features.h:
```

```
/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-  
openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/cdefs.h:
```


/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/wordsize.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/gnu/stubs.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/include/stddef.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/types.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/typesizes.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/libio.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/_G_config.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/wchar.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/include/stdarg.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/stdio_lim.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/sys_errlist.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/string.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/xlocale.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/stdlib.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/waitflags.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/waitstatus.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/ndian.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/ndian.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/byteswap.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/types.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/time.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/select.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/select.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/sigset.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/time.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/sysmacros.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/pthreadtypes.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/alloca.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/include/stdint.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/stdint.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/wchar.h:

partitions.h:

../../../../libblkid/src/blkidP.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/dirent.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/dirent.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/posix1_lim.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/local_lim.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/linux/limits.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/stat.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/stat.h:

../../../../include/c.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/include-fixed/limits.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/include-fixed/syslimits.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/limits.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/posix2_lim.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/xopen_lim.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/errno.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/errno.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/linux/errno.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm/errno.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm-generic/errno.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm-generic/errno-base.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/err.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/fcntl.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/fcntl.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/uio.h:

../../../../include/bitops.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/param.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/linux/param.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm/param.h:

../../../../include/blkdev.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/ioctl.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/ioctls.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm/ioctls.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm-generic/ioctls.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-

openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/linux/ioctl.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm/ioctl.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/asm-generic/ioctl.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/ioctl-types.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/sys/ttydefaults.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/unistd.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/posix_opt.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/environments.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/bits/confname.h:

/home/shiva/mar15/sdk-openwrt/staging_dir/toolchain-arm_v7-a_gcc-4.5-linaro_glibc-2.14.1_eabi/lib/gcc/arm-openwrt-linux-gnueabi/4.5.4/../../../../arm-openwrt-linux-gnueabi/sys-include/getopt.h:

../../../../libblkid/src/blkid.h:

../../../../include/list.h:

blkid_parttypes.h:

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/*

* Fallback for old or obscure libcs without dynamically allocated cpusets

*

* The following macros are based on code from glibc.

*

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The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and

modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

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- a) The modified work must itself be a software library.
- b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.
- c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.
- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to

exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

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Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at

least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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<signature of Ty Coon>, 1 April 1990

Ty Coon, President of Vice

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Version 2, June 1991

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Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Program.

In addition, mere aggregation of another work not based on the Program with the Program (or with a work based on the Program) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may copy and distribute the Program (or a work based on it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you also do one of the following:

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b) Accompany it with a written offer, valid for at least three years, to give any third party, for a charge no more than your cost of physically performing source distribution, a complete machine-readable copy of the corresponding source code, to be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange; or,

c) Accompany it with the information you received as to the offer to distribute corresponding source code. (This alternative is allowed only for noncommercial distribution and only if you received the program in object code or executable form with such an offer, in accord with Subsection b above.)

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END OF TERMS AND CONDITIONS

Appendix: How to Apply These Terms to Your New Programs

If you develop a new program, and you want it to be of the greatest

possible use to the public, the best way to achieve this is to make it free software which everyone can redistribute and change under these terms.

To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

```
<one line to give the program's name and a brief idea of what it does.>  
Copyright (C) 19yy <name of author>
```

```
This program is free software; you can redistribute it and/or modify  
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```

```
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along with this program; if not, write to the Free Software  
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```
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Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.  
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```

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```
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```

```
<signature of Ty Coon>, 1 April 1989
```

Ty Coon, President of Vice

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That's all there is to it!

/* Align/Truncate a string in a given screen width
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0P

pP


```
WT @bsdfreebsdnetbsdopenbsdWARNING: BSD label detected on unknown (0x%x) primary partition
WARNING: ignore %d more BSD partitions
WARNING: BSD partition (%d) overflow detected, ignore
WEVGCC: (Linaro GCC 4.5-2012.02) 4.5.4 20120201 (prerelease)A.aeabi$7-A
A .,symtab.strtab.shstrtab.rel.text.data.bss.rel.data.rel.ro.local.rodata.str1.1.comment.note.GNU-
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=\s3IZbsd.c$aprobe_bsd_pt$d.LC1.LC3.LC4.LC0.LC2.LC5.LC6blkid_partitions_need_typeonlyblkid_probe_get_s
ectorblkid_probe_get_partlistblkid_partlist_get_parentblkid_partition_get_typeprintfblkid_partlist_new_parttableblk
id_is_nested_dimensionblkid_partlist_add_partitionblkid_partition_set_type_GLOBAL_OFFSET_TABLE_blkid_d
ebug_maskbsd_pt_idinfo8TdpX$@Px|
```

\$4

```
/* Align/Truncate a string in a given screen width
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```

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```
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along with this program. If not, see <http://www.gnu.org/licenses/>. */
```

```
/* tailf.c -- tail a log file and then follow it
```

```
* Created: Tue Jan 9 15:49:21 1996 by faith@acm.org
```

```
* Copyright 1996, 2003 Rickard E. Faith (faith@acm.org)
```

```
*
```

```
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```

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*

* less -F and tail -f cause a disk access every five seconds. This
* program avoids this problem by waiting for the file size to change.
* Hence, the file is not accessed, and the access time does not need to be
* flushed back to disk. This is sort of a "stealth" tail.

*/

bsd.lo - a libtool object file

Generated by libtool (GNU libtool) 2.4

#

Please DO NOT delete this file!

It is necessary for linking the library.

Name of the PIC object.

pic_object='.libs/bsd.o'

Name of the non-PIC object

non_pic_object='bsd.o'

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USE OF THIS SOFTWARE, EVEN IF NOT ADVISED OF THE POSSIBILITY OF SUCH
DAMAGE.

/*

* setpwnam.c -- edit an entry in a password database.

*

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*
* Edited 11/10/96 (DD/MM/YY ;-) by Nicolai Langfeldt (janl@math.uio.no)
* to read /etc/passwd directly so that passwd, chsh and chfn can work on
* machines that run NIS (n" YP). Changes will not be made to usernames
* starting with +.
*

1.14 libxml 2.9.1 6.1.el7.centos.3

1.14.1 Available under license :

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1.15 xar 1.6.1

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1.17 libaio 0.3.110-1

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regparse.[ch]:

enc/ascii.c

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enc/koi8_u.c

enc/shift_jis.c

enc/unicode.c

enc/us_ascii.c

enc/utf_16be.c

enc/utf_16le.c

enc/utf_32be.c

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random.c

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A C-program for MT19937, with initialization improved 2002/2/10.

Coded by Takuji Nishimura and Makoto Matsumoto.

This is a faster version by taking Shawn Cokus's optimization, Matthe Bellew's simplification, Isaku Wada's real version.

Before using, initialize the state by using `init_genrand(seed)`
or `init_by_array(init_key, key_length)`.

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* Jonathan Lange

* Robert Collins

* Andrew Bennetts

* Benjamin Peterson

* Jamu Kakar

* James Westby

* Martin [gz]

* Michael Hudson-Doyle

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`./test/python/subunit/iso8601.py`

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Files: `./test/kewpie/randgen/bench/benchmark.pl`

`./test/kewpie/randgen/gengrammar.pl`

`./test/kewpie/randgen/translateMysql.pl`

`./test/kewpie/randgen/unit/ExecutorTest.pm`

./test/kewpie/randgen/unit/FromGrammarTest.pm
./test/kewpie/randgen/unit/GendataTest.pm
./test/kewpie/randgen/unit/GrammarTest.pm
./test/kewpie/randgen/unit/IPC.pm
./test/kewpie/randgen/unit/IPC_P1.pm
./test/kewpie/randgen/unit/Metadata.pm
./test/kewpie/randgen/unit/ParseAllGrammars.pm
./test/kewpie/randgen/unit/RandomTest.pm
./test/kewpie/randgen/unit/TestMySQLServer.pm
./test/kewpie/randgen/unit/TestReplServer.pm
./test/kewpie/randgen/unit/unit.pl

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Files: ./test/kewpie/randgen/gentest.pl
./test/kewpie/randgen/lib/DBServer/DBServer.pm
./test/kewpie/randgen/lib/GenTest.pm
./test/kewpie/randgen/lib/GenTest/Properties.pm
./test/kewpie/randgen/lib/GenTest/XML/Transporter.pm
./test/kewpie/randgen/unit/Suite.pm

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Files: ./test/kewpie/randgen/lib/GenTest/Validator/DrizzleErrorLogScan.pm
./test/kewpie/randgen/lib/GenTest/Validator/MarkErrorLog.pm

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Files: ./test/kewpie/randgen/lib/GenTest/Validator/Drizzledump.pm
./test/kewpie/randgen/lib/GenTest/Validator/DrizzledumpMigrate.pm

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Files: */cmd-line-utils/libedit/config.h

*/debug/example1.c

*/debug/example2.c

*/debug/example3.c

*/debug/factorial.c

*/debug/main.c

*/debug/my_main.c

*/debug/remove_function_from_trace.pl

*/debug/tests.c

*/debug/tests-t.pl

*/extra/yassl/src/dummy.cpp

*/include/probes_mysql_nodtrace.h

*/libmysqld/resource.h

/mysql-test/

/regex/

*/sql-bench/graph-compare-results.sh

/storage/ndb/bin/

/storage/ndb/demos/

*/support-files/binary-configure.sh

*/support-files/my-huge.cnf.sh

*/support-files/my-innodb-heavy-4G.cnf.sh

*/support-files/my-large.cnf.sh

*/support-files/my-medium.cnf.sh

*/support-files/my-small.cnf.sh

*/support-files/mysqld_multi.server.sh

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Files: */BUILD/*

/client/

*/client/echo.c

*/client/get_password.c
/cmake/
*/dbug/dbug_add_tags.pl
/extra/
/include/
/libmysql/
/libmysqld/
/libservices/
*/mysql-test/include/have_perfschema.inc
*/mysql-test/include/have_perfschema.inc
*/mysql-test/lib/mtr_cases.pm
*/mysql-test/lib/mtr_gcov.pl
*/mysql-test/lib/mtr_gprof.pl
*/mysql-test/lib/mtr_io.pl
*/mysql-test/lib/mtr_match.pm
*/mysql-test/lib/mtr_misc.pl
*/mysql-test/lib/mtr_process.pl
*/mysql-test/lib/mtr_report.pm
*/mysql-test/lib/mtr_results.pm
*/mysql-test/lib/mtr_stress.pl
*/mysql-test/lib/mtr_unique.pm
*/mysql-test/lib/My/ConfigFactory.pm
*/mysql-test/lib/My/Config.pm
*/mysql-test/lib/My/CoreDump.pm
/mysql-test/lib/My/File/
*/mysql-test/lib/My/Find.pm
*/mysql-test/lib/My/Handles.pm
*/mysql-test/lib/My/Options.pm
*/mysql-test/lib/My/Platform.pm
*/mysql-test/lib/My/SafeProcess/Base.pm
*/mysql-test/lib/My/SafeProcess.pm
*/mysql-test/lib/My/SafeProcess/safe_kill_win.cc
*/mysql-test/lib/My/SafeProcess/safe_process.cc
*/mysql-test/lib/My/SafeProcess/safe_process.pl
*/mysql-test/lib/My/SafeProcess/safe_process_win.cc
*/mysql-test/lib/My/SysInfo.pm
*/mysql-test/lib/My/Test.pm
/mysql-test/lib/t/
*/mysql-test/lib/v1/mtr_cases.pl
*/mysql-test/lib/v1/mtr_gcov.pl
*/mysql-test/lib/v1/mtr_gprof.pl
*/mysql-test/lib/v1/mtr_im.pl
*/mysql-test/lib/v1/mtr_io.pl
*/mysql-test/lib/v1/mtr_match.pl
*/mysql-test/lib/v1/mtr_misc.pl
*/mysql-test/lib/v1/mtr_process.pl
*/mysql-test/lib/v1/mtr_report.pl
*/mysql-test/lib/v1/mtr_stress.pl

*/mysql-test/lib/v1/mtr_timer.pl
*/mysql-test/lib/v1/mtr_unique.pl
/mysql-test/lib/v1/My/
/mysql-test/lib/v1/My/
*/mysql-test/lib/v1/mysql-test-run.pl
*/mysql-test/mysql-stress-test.pl
*/mysql-test/mysql-test-run.pl
/mysql-test/std_data/
/mysql-test/suite/perfschema/include/
/mysql-test/suite/perfschema_stress/include/
/mysql-test/suite/perfschema_stress/include/
/mysys/
/packaging/WiX/ca/
/plugin/audit_null/
/plugin/auth/
/plugin/daemon_example/
/plugin/fulltext/
*/plugin/semisync/semisync_slave.cc
*/plugin/semisync/semisync_slave.h
/scripts/
/sql/
/sql-common/
/storage/
/strings/
*/support-files/config.huge.ini.sh
*/support-files/config.medium.ini.sh
*/support-files/config.small.ini.sh
*/support-files/MacOSX/Description.plist.sh
*/support-files/MacOSX/Info.plist.sh
*/support-files/MacOSX/StartupParameters.plist.sh
*/support-files/MySQL-shared-compat.spec.sh
*/support-files/mysql.spec.sh
*/support-files/ndb-config-2-node.ini.sh
/tests/
/unittest/
/vio/

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Files: cmd-line-utils/libedit/filecomplete.c

*/cmd-line-utils/libedit/filecomplete.h

*/cmd-line-utils/libedit/np/fgetln.c

*/cmd-line-utils/libedit/read.h

*/cmd-line-utils/libedit/readline.c

/cmd-line-utils/libedit/readline/

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Files: */client/completion_hash.h

*/scripts/mysqlaccess.sh

*/scripts/mysql_fix_extensions.sh

*/scripts/mysql_setpermission.sh

/sql-bench/

*/storage/myisam/ftbench/ft-test-run.sh

*/storage/myisam/mi_test_all.sh

*/storage/ndb/test/run-test/atrt-analyze-result.sh

*/storage/ndb/test/run-test/atrt-clear-result.sh

*/storage/ndb/test/run-test/atrt-gather-result.sh

*/storage/ndb/test/run-test/atrt-setup.sh

*/storage/ndb/test/run-test/make-config.sh
*/storage/ndb/test/run-test/make-html-reports.sh
*/storage/ndb/test/run-test/make-index.sh
*/storage/ndb/test/run-test/ndb-autotest.sh
*/strings/strxmov.c
*/strings/strxnmov.c
*/support-files/MacOSX/postflight.sh
*/support-files/MacOSX/preflight.sh
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*/storage/archive/azlib.h
/zlib/

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Files: */sql-bench/innotest1.sh
*/sql-bench/innotest1a.sh
*/sql-bench/innotest1b.sh
*/sql-bench/innotest2.sh
*/sql-bench/innotest2a.sh
*/sql-bench/innotest2b.sh

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*/storage/innobase/include/log0log.h
*/storage/innobase/include/os0sync.h
*/storage/innobase/log/log0log.c
*/storage/innobase/row/row0sel.c

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Files: */storage/innobase/btr/btr0cur.c

*/storage/innobase/buf/buf0buf.c

*/storage/innobase/include/sync0rw.h

*/storage/innobase/include/sync0sync.h

/storage/innobase/sync/

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*/storage/myisam/rt_key.c

*/storage/myisam/rt_mbr.c

*/storage/myisam/rt_mbr.h

*/storage/myisam/sp_defs.h

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*/storage/innobase/trx/trx0rseg.c

*/storage/innobase/ut/ut0bh.c

*/storage/innobase/ut/ut0ut.c

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*/plugin/semisync/semisync.h

*/plugin/semisync/semisync_slave_plugin.cc

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*/strings/ctype-eucjpms.c

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Files: */scripts/mysqld_safe.sh

*/support-files/mysql-multi.server.sh

*/support-files/mysql.server.sh

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mysql-test\t\license_basic.test


```

#                                     #
# Variable Name: license                #
# Scope: Global                         #
# Access Type: Static                   #
# Data Type: string                     #
#                                     #
#                                     #
# Creation Date: 2008-02-07             #
# Author : Sharique Abdullah            #
#                                     #
#                                     #
# Description:Test Cases of Dynamic System Variable license      #
#      that checks the behavior of this variable in the following ways #
#      * Value Check                    #
#      * Scope Check                    #
#                                     #
# Reference: http://dev.mysql.com/doc/refman/5.1/en/              #
# server-system-variables.html                    #
#                                     #
#####

--echo '#-----BS_STVARS_040_01-----#'
#####
#  Displaying default value                #
#####
SELECT COUNT(@@GLOBAL.license);
--echo 1 Expected

--echo '#-----BS_STVARS_040_02-----#'
#####
#  Check if Value can set                  #
#####

--error ER_INCORRECT_GLOBAL_LOCAL_VAR
SET @@GLOBAL.license=1;
--echo Expected error 'Read only variable'

SELECT COUNT(@@GLOBAL.license);
--echo 1 Expected

--echo '#-----BS_STVARS_040_03-----#'
#####
# Check if the value in GLOBAL Table matches value in variable #
#####

```

```

--disable_warnings
SELECT @@GLOBAL.license = VARIABLE_VALUE
FROM INFORMATION_SCHEMA.GLOBAL_VARIABLES
WHERE VARIABLE_NAME='license';
--enable_warnings
--echo 1 Expected

SELECT COUNT(@@GLOBAL.license);
--echo 1 Expected

--disable_warnings
SELECT COUNT(VARIABLE_VALUE)
FROM INFORMATION_SCHEMA.GLOBAL_VARIABLES
WHERE VARIABLE_NAME='license';
--enable_warnings
--echo 1 Expected

--echo '#-----BS_STVARS_040_04-----#'
#####
# Check if accessing variable with and without GLOBAL point to same variable #
#####
SELECT @@license = @@GLOBAL.license;
--echo 1 Expected

--echo '#-----BS_STVARS_040_05-----#'
#####
# Check if license can be accessed with and without @@ sign #
#####

SELECT COUNT(@@license);
--echo 1 Expected

--Error ER_INCORRECT_GLOBAL_LOCAL_VAR
SELECT COUNT(@@local.license);
--echo Expected error 'Variable is a GLOBAL variable'

--Error ER_INCORRECT_GLOBAL_LOCAL_VAR
SELECT COUNT(@@SESSION.license);
--echo Expected error 'Variable is a GLOBAL variable'

SELECT COUNT(@@GLOBAL.license);
--echo 1 Expected

```

```
--Error ER_BAD_FIELD_ERROR
SELECT license = @@SESSION.license;
--echo Expected error 'Readonly variable'
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```

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```
#-----BS_STVARS_040_01-----#
```

```
SELECT COUNT(@@GLOBAL.license);  
COUNT(@@GLOBAL.license)
```

1

1 Expected

```
#-----BS_STVARS_040_02-----#
```

```
SET @@GLOBAL.license=1;  
ERROR HY000: Variable 'license' is a read only variable  
Expected error 'Read only variable'  
SELECT COUNT(@@GLOBAL.license);  
COUNT(@@GLOBAL.license)
```

1

1 Expected

```
#-----BS_STVARS_040_03-----#
```

```
SELECT @@GLOBAL.license = VARIABLE_VALUE  
FROM INFORMATION_SCHEMA.GLOBAL_VARIABLES  
WHERE VARIABLE_NAME='license';  
@@GLOBAL.license = VARIABLE_VALUE
```

1

1 Expected

```
SELECT COUNT(@@GLOBAL.license);  
COUNT(@@GLOBAL.license)
```

1

1 Expected

```
SELECT COUNT(VARIABLE_VALUE)  
FROM INFORMATION_SCHEMA.GLOBAL_VARIABLES  
WHERE VARIABLE_NAME='license';  
COUNT(VARIABLE_VALUE)
```

1

1 Expected

```

'#-----BS_STVARS_040_04-----#'
SELECT @@license = @@GLOBAL.license;
@@license = @@GLOBAL.license
1
1 Expected
'#-----BS_STVARS_040_05-----#'
SELECT COUNT(@@license);
COUNT(@@license)
1
1 Expected
SELECT COUNT(@@local.license);
ERROR HY000: Variable 'license' is a GLOBAL variable
Expected error 'Variable is a GLOBAL variable'
SELECT COUNT(@@SESSION.license);
ERROR HY000: Variable 'license' is a GLOBAL variable
Expected error 'Variable is a GLOBAL variable'
SELECT COUNT(@@GLOBAL.license);
COUNT(@@GLOBAL.license)
1
1 Expected
SELECT license = @@SESSION.license;
ERROR 42S22: Unknown column 'license' in 'field list'
Expected error 'Readonly variable'
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```

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1.20 chef 11.8.2

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1.24 utf-bom-utils 1.0

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1.25 jansson 2.7 1+deb8u1

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Upstream-Contact: Petri Lehtinen <petri@digip.org>

Source: <https://github.com/akheron/jansson>

Files: *

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1.26 xmlsec 1.2.20 7.el7_4

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1.28 python-setuptools 0.6c11

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A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see <http://www.opensource.org> for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

Release from	Derived from	Year	Owner	GPL-compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes

2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes

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regenc.[ch]:

regerror.c:

regex.c:

regint.h:

regparse.[ch]:

enc/ascii.c

enc/big5.c

enc/cp949.c

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enc/encdb.c
enc/euc_jp.c
enc/euc_kr.c
enc/euc_tw.c
enc/gb18030.c
enc/gb2312.c
enc/gbk.c
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enc/iso_8859_11.c
enc/iso_8859_13.c
enc/iso_8859_14.c
enc/iso_8859_15.c
enc/iso_8859_16.c
enc/iso_8859_2.c
enc/iso_8859_3.c
enc/iso_8859_4.c
enc/iso_8859_5.c
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random.c

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A C-program for MT19937, with initialization improved 2002/2/10.

Coded by Takuji Nishimura and Makoto Matsumoto.

This is a faster version by taking Shawn Cokus's optimization,

Matthe Bellew's simplification, Isaku Wada's real version.

Before using, initialize the state by using `init_genrand(seed)`

or `init_by_array(init_key, key_length)`.

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ext/digest/sha1/sha1.[ch]:

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That's all there is to it!

1.35 free-type 2.4.11

1.36 pixman 0.32.6

1.36.1 Available under license :

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*

* Author: Siarhei Siamashka (siarhei.siamashka@nokia.com)

*/

/*

* This file contains a macro ('generate_composite_function') which can
* construct 2D image processing functions, based on a common template.
* Any combinations of source, destination and mask images with 8bpp,
* 16bpp, 24bpp, 32bpp color formats are supported.

*

* This macro takes care of:

- * - handling of leading and trailing unaligned pixels
- * - doing most of the work related to L2 cache preload
- * - encourages the use of software pipelining for better instructions
* scheduling

*

* The user of this macro has to provide some configuration parameters
* (bit depths for the images, prefetch distance, etc.) and a set of
* macros, which should implement basic code chunks responsible for
* pixels processing. See 'pixman-arm-neon-asm.S' file for the usage
* examples.

*

* TODO:

- * - try overlapped pixel method (from Ian Rickards) when processing
* exactly two blocks of pixels
- * - maybe add an option to do reverse scanline processing

```

*/

/*
* Bit flags for 'generate_composite_function' macro which are used
* to tune generated functions behavior.
*/
.set FLAG_DST_WRITEONLY,    0
.set FLAG_DST_READWRITE,   1
.set FLAG_DEINTERLEAVE_32BPP, 2

/*
* Offset in stack where mask and source pointer/stride can be accessed
* from 'init' macro. This is useful for doing special handling for solid mask.
*/
.set ARGS_STACK_OFFSET,    40

/*
* Constants for selecting preferable prefetch type.
*/
.set PREFETCH_TYPE_NONE,    0 /* No prefetch at all */
.set PREFETCH_TYPE_SIMPLE,  1 /* A simple, fixed-distance-ahead prefetch */
.set PREFETCH_TYPE_ADVANCED, 2 /* Advanced fine-grained prefetch */

/*
* Definitions of supplementary pixld/pixst macros (for partial load/store of
* pixel data).
*/

.macro pixldst1 op, elem_size, reg1, mem_operand, abits
.if abits > 0
    op&.&elem_size {d&reg1}, [&mem_operand&, :&abits&]!
.else
    op&.&elem_size {d&reg1}, [&mem_operand&]!
.endif
.endm

.macro pixldst2 op, elem_size, reg1, reg2, mem_operand, abits
.if abits > 0
    op&.&elem_size {d&reg1, d&reg2}, [&mem_operand&, :&abits&]!
.else
    op&.&elem_size {d&reg1, d&reg2}, [&mem_operand&]!
.endif
.endm

.macro pixldst4 op, elem_size, reg1, reg2, reg3, reg4, mem_operand, abits
.if abits > 0
    op&.&elem_size {d&reg1, d&reg2, d&reg3, d&reg4}, [&mem_operand&, :&abits&]!
.else

```

```

    op&.&elem_size {d&reg1, d&reg2, d&reg3, d&reg4}, [&mem_operand&]!
.endif
.endm

.macro pixldst0 op, elem_size, reg1, idx, mem_operand, abits
    op&.&elem_size {d&reg1[idx]}, [&mem_operand&]!
.endm

.macro pixldst3 op, elem_size, reg1, reg2, reg3, mem_operand
    op&.&elem_size {d&reg1, d&reg2, d&reg3}, [&mem_operand&]!
.endm

.macro pixldst30 op, elem_size, reg1, reg2, reg3, idx, mem_operand
    op&.&elem_size {d&reg1[idx], d&reg2[idx], d&reg3[idx]}, [&mem_operand&]!
.endm

.macro pixldst numbytes, op, elem_size, basereg, mem_operand, abits
.if numbytes == 32
    pixldst4 op, elem_size, %(basereg+4), %(basereg+5), \
        %(basereg+6), %(basereg+7), mem_operand, abits
.elseif numbytes == 16
    pixldst2 op, elem_size, %(basereg+2), %(basereg+3), mem_operand, abits
.elseif numbytes == 8
    pixldst1 op, elem_size, %(basereg+1), mem_operand, abits
.elseif numbytes == 4
    .if !RESPECT_STRICT_ALIGNMENT || (elem_size == 32)
        pixldst0 op, 32, %(basereg+0), 1, mem_operand, abits
    .elseif elem_size == 16
        pixldst0 op, 16, %(basereg+0), 2, mem_operand, abits
        pixldst0 op, 16, %(basereg+0), 3, mem_operand, abits
    .else
        pixldst0 op, 8, %(basereg+0), 4, mem_operand, abits
        pixldst0 op, 8, %(basereg+0), 5, mem_operand, abits
        pixldst0 op, 8, %(basereg+0), 6, mem_operand, abits
        pixldst0 op, 8, %(basereg+0), 7, mem_operand, abits
    .endif
.elseif numbytes == 2
    .if !RESPECT_STRICT_ALIGNMENT || (elem_size == 16)
        pixldst0 op, 16, %(basereg+0), 1, mem_operand, abits
    .else
        pixldst0 op, 8, %(basereg+0), 2, mem_operand, abits
        pixldst0 op, 8, %(basereg+0), 3, mem_operand, abits
    .endif
.elseif numbytes == 1
    pixldst0 op, 8, %(basereg+0), 1, mem_operand, abits
.else
    .error "unsupported size: numbytes"
.endif

```

```

.endm

.macro pixld numpix, bpp, basereg, mem_operand, abits=0
.if bpp > 0
.if (bpp == 32) && (numpix == 8) && (DEINTERLEAVE_32BPP_ENABLED != 0)
    pixldst4 vld4, 8, %(basereg+4), %(basereg+5), \
        %(basereg+6), %(basereg+7), mem_operand, abits
.elseif (bpp == 24) && (numpix == 8)
    pixldst3 vld3, 8, %(basereg+3), %(basereg+4), %(basereg+5), mem_operand
.elseif (bpp == 24) && (numpix == 4)
    pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 4, mem_operand
    pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 5, mem_operand
    pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 6, mem_operand
    pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 7, mem_operand
.elseif (bpp == 24) && (numpix == 2)
    pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 2, mem_operand
    pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 3, mem_operand
.elseif (bpp == 24) && (numpix == 1)
    pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 1, mem_operand
.else
    pixldst %(numpix * bpp / 8), vld1, %(bpp), basereg, mem_operand, abits
.endif
.endif
.endm

```

```

.macro pixst numpix, bpp, basereg, mem_operand, abits=0
.if bpp > 0
.if (bpp == 32) && (numpix == 8) && (DEINTERLEAVE_32BPP_ENABLED != 0)
    pixldst4 vst4, 8, %(basereg+4), %(basereg+5), \
        %(basereg+6), %(basereg+7), mem_operand, abits
.elseif (bpp == 24) && (numpix == 8)
    pixldst3 vst3, 8, %(basereg+3), %(basereg+4), %(basereg+5), mem_operand
.elseif (bpp == 24) && (numpix == 4)
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 4, mem_operand
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 5, mem_operand
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 6, mem_operand
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 7, mem_operand
.elseif (bpp == 24) && (numpix == 2)
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 2, mem_operand
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 3, mem_operand
.elseif (bpp == 24) && (numpix == 1)
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 1, mem_operand
.else
    pixldst %(numpix * bpp / 8), vst1, %(bpp), basereg, mem_operand, abits
.endif
.endif
.endm

```

```

.macro pixld_a numpix, bpp, basereg, mem_operand
.if (bpp * numpix) <= 128
    pixld numpix, bpp, basereg, mem_operand, %(bpp * numpix)
.else
    pixld numpix, bpp, basereg, mem_operand, 128
.endif
.endm

.macro pixst_a numpix, bpp, basereg, mem_operand
.if (bpp * numpix) <= 128
    pixst numpix, bpp, basereg, mem_operand, %(bpp * numpix)
.else
    pixst numpix, bpp, basereg, mem_operand, 128
.endif
.endm

/*
* Pixel fetcher for nearest scaling (needs TMP1, TMP2, VX, UNIT_X register
* aliases to be defined)
*/

.macro pixld1_s elem_size, reg1, mem_operand
.if elem_size == 16
    mov    TMP1, VX, asr #16
    adds  VX, VX, UNIT_X
5: subpls VX, VX, SRC_WIDTH_FIXED
    bpl   5b
    add   TMP1, mem_operand, TMP1, asl #1
    mov   TMP2, VX, asr #16
    adds  VX, VX, UNIT_X
5: subpls VX, VX, SRC_WIDTH_FIXED
    bpl   5b
    add   TMP2, mem_operand, TMP2, asl #1
    vld1.16 {d&reg1&[0]}, [TMP1, :16]
    mov   TMP1, VX, asr #16
    adds  VX, VX, UNIT_X
5: subpls VX, VX, SRC_WIDTH_FIXED
    bpl   5b
    add   TMP1, mem_operand, TMP1, asl #1
    vld1.16 {d&reg1&[1]}, [TMP2, :16]
    mov   TMP2, VX, asr #16
    adds  VX, VX, UNIT_X
5: subpls VX, VX, SRC_WIDTH_FIXED
    bpl   5b
    add   TMP2, mem_operand, TMP2, asl #1
    vld1.16 {d&reg1&[2]}, [TMP1, :16]
    vld1.16 {d&reg1&[3]}, [TMP2, :16]
.elseif elem_size == 32
    mov   TMP1, VX, asr #16

```

```

    adds    VX, VX, UNIT_X
5: subpls  VX, VX, SRC_WIDTH_FIXED
    bpl    5b
    add    TMP1, mem_operand, TMP1, asl #2
    mov    TMP2, VX, asr #16
    adds    VX, VX, UNIT_X
5: subpls  VX, VX, SRC_WIDTH_FIXED
    bpl    5b
    add    TMP2, mem_operand, TMP2, asl #2
    vld1.32 {d&reg1&[0]}, [TMP1, :32]
    vld1.32 {d&reg1&[1]}, [TMP2, :32]
.else
    .error "unsupported"
.endif
.endm

.macro pixld2_s elem_size, reg1, reg2, mem_operand
.if 0 /* elem_size == 32 */
    mov    TMP1, VX, asr #16
    add    VX, VX, UNIT_X, asl #1
    add    TMP1, mem_operand, TMP1, asl #2
    mov    TMP2, VX, asr #16
    sub    VX, VX, UNIT_X
    add    TMP2, mem_operand, TMP2, asl #2
    vld1.32 {d&reg1&[0]}, [TMP1, :32]
    mov    TMP1, VX, asr #16
    add    VX, VX, UNIT_X, asl #1
    add    TMP1, mem_operand, TMP1, asl #2
    vld1.32 {d&reg2&[0]}, [TMP2, :32]
    mov    TMP2, VX, asr #16
    add    VX, VX, UNIT_X
    add    TMP2, mem_operand, TMP2, asl #2
    vld1.32 {d&reg1&[1]}, [TMP1, :32]
    vld1.32 {d&reg2&[1]}, [TMP2, :32]
.else
    pixld1_s elem_size, reg1, mem_operand
    pixld1_s elem_size, reg2, mem_operand
.endif
.endm

.macro pixld0_s elem_size, reg1, idx, mem_operand
.if elem_size == 16
    mov    TMP1, VX, asr #16
    adds    VX, VX, UNIT_X
5: subpls  VX, VX, SRC_WIDTH_FIXED
    bpl    5b
    add    TMP1, mem_operand, TMP1, asl #1
    vld1.16 {d&reg1&[idx]}, [TMP1, :16]

```



```

.elseif elem_size == 32
    mov    TMP1, VX, asr #16
    adds  VX, VX, UNIT_X
5: subpls VX, VX, SRC_WIDTH_FIXED
    bpl   5b
    add   TMP1, mem_operand, TMP1, asl #2
    vld1.32 {d&reg1&[idx]}, [TMP1, :32]
.endif
.endm

.macro pixld_s_internal numbytes, elem_size, basereg, mem_operand
.if numbytes == 32
    pixld2_s elem_size, %(basereg+4), %(basereg+5), mem_operand
    pixld2_s elem_size, %(basereg+6), %(basereg+7), mem_operand
    pixdeinterleave elem_size, %(basereg+4)
.elseif numbytes == 16
    pixld2_s elem_size, %(basereg+2), %(basereg+3), mem_operand
.elseif numbytes == 8
    pixld1_s elem_size, %(basereg+1), mem_operand
.elseif numbytes == 4
    .if elem_size == 32
        pixld0_s elem_size, %(basereg+0), 1, mem_operand
    .elseif elem_size == 16
        pixld0_s elem_size, %(basereg+0), 2, mem_operand
        pixld0_s elem_size, %(basereg+0), 3, mem_operand
    .else
        pixld0_s elem_size, %(basereg+0), 4, mem_operand
        pixld0_s elem_size, %(basereg+0), 5, mem_operand
        pixld0_s elem_size, %(basereg+0), 6, mem_operand
        pixld0_s elem_size, %(basereg+0), 7, mem_operand
    .endif
.elseif numbytes == 2
    .if elem_size == 16
        pixld0_s elem_size, %(basereg+0), 1, mem_operand
    .else
        pixld0_s elem_size, %(basereg+0), 2, mem_operand
        pixld0_s elem_size, %(basereg+0), 3, mem_operand
    .endif
.elseif numbytes == 1
    pixld0_s elem_size, %(basereg+0), 1, mem_operand
.else
    .error "unsupported size: numbytes"
.endif
.endm

.macro pixld_s numpix, bpp, basereg, mem_operand
.if bpp > 0
    pixld_s_internal %(numpix * bpp / 8), %(bpp), basereg, mem_operand

```

```

.endif
.endm

.macro vuzp8 reg1, reg2
    vuzp.8 d&reg1, d&reg2
.endm

.macro vzip8 reg1, reg2
    vzip.8 d&reg1, d&reg2
.endm

/* deinterleave B, G, R, A channels for eight 32bpp pixels in 4 registers */
.macro pixdeinterleave bpp, basereg
.if (bpp == 32) && (DEINTERLEAVE_32BPP_ENABLED != 0)
    vuzp8 %(basereg+0), %(basereg+1)
    vuzp8 %(basereg+2), %(basereg+3)
    vuzp8 %(basereg+1), %(basereg+3)
    vuzp8 %(basereg+0), %(basereg+2)
.endif
.endm

/* interleave B, G, R, A channels for eight 32bpp pixels in 4 registers */
.macro pixinterleave bpp, basereg
.if (bpp == 32) && (DEINTERLEAVE_32BPP_ENABLED != 0)
    vzip8 %(basereg+0), %(basereg+2)
    vzip8 %(basereg+1), %(basereg+3)
    vzip8 %(basereg+2), %(basereg+3)
    vzip8 %(basereg+0), %(basereg+1)
.endif
.endm

/*
* This is a macro for implementing cache preload. The main idea is that
* cache preload logic is mostly independent from the rest of pixels
* processing code. It starts at the top left pixel and moves forward
* across pixels and can jump across scanlines. Prefetch distance is
* handled in an 'incremental' way: it starts from 0 and advances to the
* optimal distance over time. After reaching optimal prefetch distance,
* it is kept constant. There are some checks which prevent prefetching
* unneeded pixel lines below the image (but it still can prefetch a bit
* more data on the right side of the image - not a big issue and may
* be actually helpful when rendering text glyphs). Additional trick is
* the use of LDR instruction for prefetch instead of PLD when moving to
* the next line, the point is that we have a high chance of getting TLB
* miss in this case, and PLD would be useless.
*
* This sounds like it may introduce a noticeable overhead (when working with
* fully cached data). But in reality, due to having a separate pipeline and

```

* instruction queue for NEON unit in ARM Cortex-A8, normal ARM code can
 * execute simultaneously with NEON and be completely shadowed by it. Thus
 * we get no performance overhead at all (*). This looks like a very nice
 * feature of Cortex-A8, if used wisely. We don't have a hardware prefetcher,
 * but still can implement some rather advanced prefetch logic in software
 * for almost zero cost!
 *
 * (*) The overhead of the prefetcher is visible when running some trivial
 * pixels processing like simple copy. Anyway, having prefetch is a must
 * when working with the graphics data.
 */

```
.macro PF a, x:vararg
.if (PREFETCH_TYPE_CURRENT == PREFETCH_TYPE_ADVANCED)
    a x
.endif
.endm

.macro cache_preload std_increment, boost_increment
.if (src_bpp_shift >= 0) || (dst_r_bpp != 0) || (mask_bpp_shift >= 0)
.if regs_shortage
    PF ldr ORIG_W, [sp] /* If we are short on regs, ORIG_W is kept on stack */
.endif
.if std_increment != 0
    PF add PF_X, PF_X, #std_increment
.endif
    PF tst PF_CTL, #0xF
    PF addne PF_X, PF_X, #boost_increment
    PF subne PF_CTL, PF_CTL, #1
    PF cmp PF_X, ORIG_W
.if src_bpp_shift >= 0
    PF pld, [PF_SRC, PF_X, lsl #src_bpp_shift]
.endif
.if dst_r_bpp != 0
    PF pld, [PF_DST, PF_X, lsl #dst_bpp_shift]
.endif
.if mask_bpp_shift >= 0
    PF pld, [PF_MASK, PF_X, lsl #mask_bpp_shift]
.endif
    PF subge PF_X, PF_X, ORIG_W
    PF subges PF_CTL, PF_CTL, #0x10
.if src_bpp_shift >= 0
    PF ldrgeb DUMMY, [PF_SRC, SRC_STRIDE, lsl #src_bpp_shift]!
.endif
.if dst_r_bpp != 0
    PF ldrgeb DUMMY, [PF_DST, DST_STRIDE, lsl #dst_bpp_shift]!
.endif
.if mask_bpp_shift >= 0
    PF ldrgeb DUMMY, [PF_MASK, MASK_STRIDE, lsl #mask_bpp_shift]!
```

```

.endif
.endif
.endm

.macro cache_preload_simple
.if (PREFETCH_TYPE_CURRENT == PREFETCH_TYPE_SIMPLE)
.if src_bpp > 0
    pld [SRC, #(PREFETCH_DISTANCE_SIMPLE * src_bpp / 8)]
.endif
.if dst_r_bpp > 0
    pld [DST_R, #(PREFETCH_DISTANCE_SIMPLE * dst_r_bpp / 8)]
.endif
.if mask_bpp > 0
    pld [MASK, #(PREFETCH_DISTANCE_SIMPLE * mask_bpp / 8)]
.endif
.endif
.endm

.macro fetch_mask_pixblock
    pixld    pixblock_size, mask_bpp, \
            (mask_basereg - pixblock_size * mask_bpp / 64), MASK
.endm

/*
* Macro which is used to process leading pixels until destination
* pointer is properly aligned (at 16 bytes boundary). When destination
* buffer uses 16bpp format, this is unnecessary, or even pointless.
*/
.macro ensure_destination_ptr_alignment process_pixblock_head, \
                                       process_pixblock_tail, \
                                       process_pixblock_tail_head
.if dst_w_bpp != 24
    tst    DST_R, #0xF
    beq    2f

.irp lowbit, 1, 2, 4, 8, 16
local skip1
.if (dst_w_bpp <= (lowbit * 8)) && ((lowbit * 8) < (pixblock_size * dst_w_bpp))
.if lowbit < 16 /* we don't need more than 16-byte alignment */
    tst    DST_R, #lowbit
    beq    1f
.endif
    pixld_src (lowbit * 8 / dst_w_bpp), src_bpp, src_basereg, SRC
    pixld    (lowbit * 8 / dst_w_bpp), mask_bpp, mask_basereg, MASK
.if dst_r_bpp > 0
    pixld_a (lowbit * 8 / dst_r_bpp), dst_r_bpp, dst_r_basereg, DST_R
.else
    add    DST_R, DST_R, #lowbit

```

```

.endif
    PF add    PF_X, PF_X, #(lowbit * 8 / dst_w_bpp)
    sub      W, W, #(lowbit * 8 / dst_w_bpp)
1:
.endif
.endr

    pixdeinterleave src_bpp, src_basereg
    pixdeinterleave mask_bpp, mask_basereg
    pixdeinterleave dst_r_bpp, dst_r_basereg

    process_pixblock_head
    cache_preload 0, pixblock_size
    cache_preload_simple
    process_pixblock_tail

    pixinterleave dst_w_bpp, dst_w_basereg
.irp lowbit, 1, 2, 4, 8, 16
.if (dst_w_bpp <= (lowbit * 8)) && ((lowbit * 8) < (pixblock_size * dst_w_bpp))
.if lowbit < 16 /* we don't need more than 16-byte alignment */
    tst     DST_W, #lowbit
    beq     1f
.endif
    pixst_a (lowbit * 8 / dst_w_bpp), dst_w_bpp, dst_w_basereg, DST_W
1:
.endif
.endr
.endif
2:
.endm

/*
 * Special code for processing up to (pixblock_size - 1) remaining
 * trailing pixels. As SIMD processing performs operation on
 * pixblock_size pixels, anything smaller than this has to be loaded
 * and stored in a special way. Loading and storing of pixel data is
 * performed in such a way that we fill some 'slots' in the NEON
 * registers (some slots naturally are unused), then perform compositing
 * operation as usual. In the end, the data is taken from these 'slots'
 * and saved to memory.
 *
 * cache_preload_flag - allows to suppress prefetch if
 *                     set to 0
 * dst_aligned_flag   - selects whether destination buffer
 *                     is aligned
 */
.macro process_trailing_pixels cache_preload_flag, \
                               dst_aligned_flag, \
                               process_pixblock_head, \

```

```

                process_pixblock_tail, \
                process_pixblock_tail_head
    tst     W, #(pixblock_size - 1)
    beq     2f
.irp chunk_size, 16, 8, 4, 2, 1
.if pixblock_size > chunk_size
    tst     W, #chunk_size
    beq     1f
    pixld_src  chunk_size, src_bpp, src_basereg, SRC
    pixld     chunk_size, mask_bpp, mask_basereg, MASK
.if dst_aligned_flag != 0
    pixld_a   chunk_size, dst_r_bpp, dst_r_basereg, DST_R
.else
    pixld     chunk_size, dst_r_bpp, dst_r_basereg, DST_R
.endif
.if cache_preload_flag != 0
    PF add    PF_X, PF_X, #chunk_size
.endif
1:
.endif
.endr

    pixdeinterleave src_bpp, src_basereg
    pixdeinterleave mask_bpp, mask_basereg
    pixdeinterleave dst_r_bpp, dst_r_basereg

    process_pixblock_head
.if cache_preload_flag != 0
    cache_preload 0, pixblock_size
    cache_preload_simple
.endif
    process_pixblock_tail
    pixinterleave dst_w_bpp, dst_w_basereg
.irp chunk_size, 16, 8, 4, 2, 1
.if pixblock_size > chunk_size
    tst     W, #chunk_size
    beq     1f
.if dst_aligned_flag != 0
    pixst_a   chunk_size, dst_w_bpp, dst_w_basereg, DST_W
.else
    pixst     chunk_size, dst_w_bpp, dst_w_basereg, DST_W
.endif
1:
.endif
.endif
2:
.endm

/*

```

```

* Macro, which performs all the needed operations to switch to the next
* scanline and start the next loop iteration unless all the scanlines
* are already processed.
*/
.macro advance_to_next_scanline start_of_loop_label
.if regs_shortage
    ldrd    W, [sp] /* load W and H (width and height) from stack */
.else
    mov     W, ORIG_W
.endif
    add     DST_W, DST_W, DST_STRIDE, lsl #dst_bpp_shift
.if src_bpp != 0
    add     SRC, SRC, SRC_STRIDE, lsl #src_bpp_shift
.endif
.if mask_bpp != 0
    add     MASK, MASK, MASK_STRIDE, lsl #mask_bpp_shift
.endif
.if (dst_w_bpp != 24)
    sub     DST_W, DST_W, W, lsl #dst_bpp_shift
.endif
.if (src_bpp != 24) && (src_bpp != 0)
    sub     SRC, SRC, W, lsl #src_bpp_shift
.endif
.if (mask_bpp != 24) && (mask_bpp != 0)
    sub     MASK, MASK, W, lsl #mask_bpp_shift
.endif
    subs    H, H, #1
    mov     DST_R, DST_W
.if regs_shortage
    str     H, [sp, #4] /* save updated height to stack */
.endif
    bge     start_of_loop_label
.endm

/*
* Registers are allocated in the following way by default:
* d0, d1, d2, d3 - reserved for loading source pixel data
* d4, d5, d6, d7 - reserved for loading destination pixel data
* d24, d25, d26, d27 - reserved for loading mask pixel data
* d28, d29, d30, d31 - final destination pixel data for writeback to memory
*/
.macro generate_composite_function fname, \
    src_bpp_, \
    mask_bpp_, \
    dst_w_bpp_, \
    flags, \
    pixblock_size_, \
    prefetch_distance, \

```

```

        init, \
        cleanup, \
        process_pixblock_head, \
        process_pixblock_tail, \
        process_pixblock_tail_head, \
        dst_w_basereg_ = 28, \
        dst_r_basereg_ = 4, \
        src_basereg_ = 0, \
        mask_basereg_ = 24

.func fname
.global fname
/* For ELF format also set function visibility to hidden */
#ifdef __ELF__
.hidden fname
.type fname, %function
#endif
fname:
    push    {r4-r12, lr}    /* save all registers */

/*
 * Select prefetch type for this function. If prefetch distance is
 * set to 0 or one of the color formats is 24bpp, SIMPLE prefetch
 * has to be used instead of ADVANCED.
 */
    .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_DEFAULT
.if prefetch_distance == 0
    .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_NONE
.elseif (PREFETCH_TYPE_CURRENT > PREFETCH_TYPE_SIMPLE) && \
        ((src_bpp_ == 24) || (mask_bpp_ == 24) || (dst_w_bpp_ == 24))
    .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_SIMPLE
.endif

/*
 * Make some macro arguments globally visible and accessible
 * from other macros
 */
    .set src_bpp, src_bpp_
    .set mask_bpp, mask_bpp_
    .set dst_w_bpp, dst_w_bpp_
    .set pixblock_size, pixblock_size_
    .set dst_w_basereg, dst_w_basereg_
    .set dst_r_basereg, dst_r_basereg_
    .set src_basereg, src_basereg_
    .set mask_basereg, mask_basereg_

    .macro pixld_src x:vararg
        pixld x

```



```

.endm
.macro fetch_src_pixblock
    pixld_src  pixblock_size, src_bpp, \
        (src_basereg - pixblock_size * src_bpp / 64), SRC
.endm
/*
* Assign symbolic names to registers
*/
W      .req    r0    /* width (is updated during processing) */
H      .req    r1    /* height (is updated during processing) */
DST_W  .req    r2    /* destination buffer pointer for writes */
DST_STRIDE .req    r3    /* destination image stride */
SRC     .req    r4    /* source buffer pointer */
SRC_STRIDE .req    r5    /* source image stride */
DST_R   .req    r6    /* destination buffer pointer for reads */

MASK    .req    r7    /* mask pointer */
MASK_STRIDE .req    r8    /* mask stride */

PF_CTL  .req    r9    /* combined lines counter and prefetch */
                /* distance increment counter */
PF_X    .req    r10   /* pixel index in a scanline for current */
                /* pretetch position */
PF_SRC  .req    r11   /* pointer to source scanline start */
                /* for prefetch purposes */
PF_DST  .req    r12   /* pointer to destination scanline start */
                /* for prefetch purposes */
PF_MASK .req    r14   /* pointer to mask scanline start */
                /* for prefetch purposes */

/*
* Check whether we have enough registers for all the local variables.
* If we don't have enough registers, original width and height are
* kept on top of stack (and 'regs_shortage' variable is set to indicate
* this for the rest of code). Even if there are enough registers, the
* allocation scheme may be a bit different depending on whether source
* or mask is not used.
*/
.if (PREFETCH_TYPE_CURRENT < PREFETCH_TYPE_ADVANCED)
    ORIG_W  .req    r10    /* saved original width */
    DUMMY   .req    r12    /* temporary register */
    .set    regs_shortage, 0
.elseif mask_bpp == 0
    ORIG_W  .req    r7     /* saved original width */
    DUMMY   .req    r8     /* temporary register */
    .set    regs_shortage, 0
.elseif src_bpp == 0
    ORIG_W  .req    r4     /* saved original width */
    DUMMY   .req    r5     /* temporary register */

```

```

.set    regs_shortage, 0
.else
    ORIG_W    .req    r1    /* saved original width */
    DUMMY     .req    r1    /* temporary register */
.set    regs_shortage, 1
.endif

.set mask_bpp_shift, -1
.if src_bpp == 32
    .set src_bpp_shift, 2
.elseif src_bpp == 24
    .set src_bpp_shift, 0
.elseif src_bpp == 16
    .set src_bpp_shift, 1
.elseif src_bpp == 8
    .set src_bpp_shift, 0
.elseif src_bpp == 0
    .set src_bpp_shift, -1
.else
    .error "requested src bpp (src_bpp) is not supported"
.endif
.if mask_bpp == 32
    .set mask_bpp_shift, 2
.elseif mask_bpp == 24
    .set mask_bpp_shift, 0
.elseif mask_bpp == 8
    .set mask_bpp_shift, 0
.elseif mask_bpp == 0
    .set mask_bpp_shift, -1
.else
    .error "requested mask bpp (mask_bpp) is not supported"
.endif
.if dst_w_bpp == 32
    .set dst_bpp_shift, 2
.elseif dst_w_bpp == 24
    .set dst_bpp_shift, 0
.elseif dst_w_bpp == 16
    .set dst_bpp_shift, 1
.elseif dst_w_bpp == 8
    .set dst_bpp_shift, 0
.else
    .error "requested dst bpp (dst_w_bpp) is not supported"
.endif

.if (((flags) & FLAG_DST_READWRITE) != 0)
    .set dst_r_bpp, dst_w_bpp
.else
    .set dst_r_bpp, 0

```

```

.endif
.if (((flags) & FLAG_DEINTERLEAVE_32BPP) != 0)
    .set DEINTERLEAVE_32BPP_ENABLED, 1
.else
    .set DEINTERLEAVE_32BPP_ENABLED, 0
.endif

.if prefetch_distance < 0 || prefetch_distance > 15
    .error "invalid prefetch distance (prefetch_distance)"
.endif

.if src_bpp > 0
    ldr    SRC, [sp, #40]
.endif
.if mask_bpp > 0
    ldr    MASK, [sp, #48]
.endif
    PF mov    PF_X, #0
.if src_bpp > 0
    ldr    SRC_STRIDE, [sp, #44]
.endif
.if mask_bpp > 0
    ldr    MASK_STRIDE, [sp, #52]
.endif
    mov    DST_R, DST_W

.if src_bpp == 24
    sub    SRC_STRIDE, SRC_STRIDE, W
    sub    SRC_STRIDE, SRC_STRIDE, W, lsl #1
.endif
.if mask_bpp == 24
    sub    MASK_STRIDE, MASK_STRIDE, W
    sub    MASK_STRIDE, MASK_STRIDE, W, lsl #1
.endif
.if dst_w_bpp == 24
    sub    DST_STRIDE, DST_STRIDE, W
    sub    DST_STRIDE, DST_STRIDE, W, lsl #1
.endif

/*
* Setup advanced prefetcher initial state
*/
    PF mov    PF_SRC, SRC
    PF mov    PF_DST, DST_R
    PF mov    PF_MASK, MASK
    /* PF_CTL = prefetch_distance | ((h - 1) << 4) */
    PF mov    PF_CTL, H, lsl #4
    PF add    PF_CTL, #(prefetch_distance - 0x10)

```

```

init
.if regs_shortage
push    {r0, r1}
.endif
subs    H, H, #1
.if regs_shortage
str     H, [sp, #4] /* save updated height to stack */
.else
mov     ORIG_W, W
.endif
blt     9f
cmp     W, #(pixblock_size * 2)
blt     8f
/*
* This is the start of the pipelined loop, which if optimized for
* long scanlines
*/
0:
ensure_destination_ptr_alignment process_pixblock_head, \
                                process_pixblock_tail, \
                                process_pixblock_tail_head

/* Implement "head (tail_head) ... (tail_head) tail" loop pattern */
pixld_a  pixblock_size, dst_r_bpp, \
         (dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
fetch_src_pixblock
pixld    pixblock_size, mask_bpp, \
         (mask_basereg - pixblock_size * mask_bpp / 64), MASK
PF add   PF_X, PF_X, #pixblock_size
process_pixblock_head
cache_preload 0, pixblock_size
cache_preload_simple
subs     W, W, #(pixblock_size * 2)
blt     2f
1:
process_pixblock_tail_head
cache_preload_simple
subs     W, W, #pixblock_size
bge     1b
2:
process_pixblock_tail
pixst_a  pixblock_size, dst_w_bpp, \
         (dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W

/* Process the remaining trailing pixels in the scanline */
process_trailing_pixels 1, 1, \
                        process_pixblock_head, \

```

```

        process_pixblock_tail, \
        process_pixblock_tail_head
advance_to_next_scanline 0b

.if regs_shortage
    pop    {r0, r1}
.endif
cleanup
pop    {r4-r12, pc} /* exit */
/*
* This is the start of the loop, designed to process images with small width
* (less than pixblock_size * 2 pixels). In this case neither pipelining
* nor prefetch are used.
*/
8:
/* Process exactly pixblock_size pixels if needed */
tst    W, #pixblock_size
beq    1f
pixld  pixblock_size, dst_r_bpp, \
        (dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
fetch_src_pixblock
pixld  pixblock_size, mask_bpp, \
        (mask_basereg - pixblock_size * mask_bpp / 64), MASK
process_pixblock_head
process_pixblock_tail
pixst  pixblock_size, dst_w_bpp, \
        (dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W
1:
/* Process the remaining trailing pixels in the scanline */
process_trailing_pixels 0, 0, \
        process_pixblock_head, \
        process_pixblock_tail, \
        process_pixblock_tail_head
advance_to_next_scanline 8b
9:
.if regs_shortage
    pop    {r0, r1}
.endif
cleanup
pop    {r4-r12, pc} /* exit */

.purgem  fetch_src_pixblock
.purgem  pixld_src

.unreq  SRC
.unreq  MASK
.unreq  DST_R
.unreq  DST_W

```

```

.unreq ORIG_W
.unreq W
.unreq H
.unreq SRC_STRIDE
.unreq DST_STRIDE
.unreq MASK_STRIDE
.unreq PF_CTL
.unreq PF_X
.unreq PF_SRC
.unreq PF_DST
.unreq PF_MASK
.unreq DUMMY
.endfunc
.endm

/*
* A simplified variant of function generation template for a single
* scanline processing (for implementing pixman combine functions)
*/
.macro generate_composite_function_scanline use_nearest_scaling, \
        fname, \
        src_bpp_, \
        mask_bpp_, \
        dst_w_bpp_, \
        flags, \
        pixblock_size_, \
        init, \
        cleanup, \
        process_pixblock_head, \
        process_pixblock_tail, \
        process_pixblock_tail_head, \
        dst_w_basereg_ = 28, \
        dst_r_basereg_ = 4, \
        src_basereg_ = 0, \
        mask_basereg_ = 24

.func fname
.global fname
/* For ELF format also set function visibility to hidden */
#ifdef __ELF__
.hidden fname
.type fname, %function
#endif
fname:
.set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_NONE
/*
* Make some macro arguments globally visible and accessible
* from other macros

```

```

*/
.set src_bpp, src_bpp_
.set mask_bpp, mask_bpp_
.set dst_w_bpp, dst_w_bpp_
.set pixblock_size, pixblock_size_
.set dst_w_basereg, dst_w_basereg_
.set dst_r_basereg, dst_r_basereg_
.set src_basereg, src_basereg_
.set mask_basereg, mask_basereg_

.if use_nearest_scaling != 0
/*
 * Assign symbolic names to registers for nearest scaling
 */
W      .req    r0
DST_W  .req    r1
SRC     .req    r2
VX     .req    r3
UNIT_X  .req    ip
MASK   .req    lr
TMP1   .req    r4
TMP2   .req    r5
DST_R  .req    r6
SRC_WIDTH_FIXED .req    r7

.macro pixld_src x:vararg
    pixld_s x
.endm

ldr    UNIT_X, [sp]
push   {r4-r8, lr}
ldr    SRC_WIDTH_FIXED, [sp, #(24 + 4)]
.if mask_bpp != 0
ldr    MASK, [sp, #(24 + 8)]
.endif
.else
/*
 * Assign symbolic names to registers
 */
W      .req    r0 /* width (is updated during processing) */
DST_W  .req    r1 /* destination buffer pointer for writes */
SRC     .req    r2 /* source buffer pointer */
DST_R  .req    ip /* destination buffer pointer for reads */
MASK   .req    r3 /* mask pointer */

.macro pixld_src x:vararg
    pixld x
.endm

```

```

.endif

.if (((flags) & FLAG_DST_READWRITE) != 0)
    .set dst_r_bpp, dst_w_bpp
.else
    .set dst_r_bpp, 0
.endif

.if (((flags) & FLAG_DEINTERLEAVE_32BPP) != 0)
    .set DEINTERLEAVE_32BPP_ENABLED, 1
.else
    .set DEINTERLEAVE_32BPP_ENABLED, 0
.endif

.macro fetch_src_pixblock
    pixld_src    pixblock_size, src_bpp, \
                (src_basereg - pixblock_size * src_bpp / 64), SRC
.endm

init
mov    DST_R, DST_W

cmp    W, #pixblock_size
blt    8f

ensure_destination_ptr_alignment process_pixblock_head, \
                                process_pixblock_tail, \
                                process_pixblock_tail_head

subs   W, W, #pixblock_size
blt    7f

/* Implement "head (tail_head) ... (tail_head) tail" loop pattern */
pixld_a    pixblock_size, dst_r_bpp, \
            (dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
fetch_src_pixblock
pixld    pixblock_size, mask_bpp, \
        (mask_basereg - pixblock_size * mask_bpp / 64), MASK
process_pixblock_head
subs    W, W, #pixblock_size
blt    2f
1:
process_pixblock_tail_head
subs    W, W, #pixblock_size
bge    1b
2:
process_pixblock_tail
pixst_a    pixblock_size, dst_w_bpp, \
            (dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W

```


7:

```
/* Process the remaining trailing pixels in the scanline (dst aligned) */  
process_trailing_pixels 0, 1, \  
    process_pixblock_head, \  
    process_pixblock_tail, \  
    process_pixblock_tail_head
```

cleanup

```
.if use_nearest_scaling != 0  
    pop    {r4-r8, pc} /* exit */  
.else  
    bx    lr /* exit */  
.endif
```

8:

```
/* Process the remaining trailing pixels in the scanline (dst unaligned) */  
process_trailing_pixels 0, 0, \  
    process_pixblock_head, \  
    process_pixblock_tail, \  
    process_pixblock_tail_head
```

cleanup

```
.if use_nearest_scaling != 0  
    pop    {r4-r8, pc} /* exit */
```

```
.unreq    DST_R  
.unreq    SRC  
.unreq    W  
.unreq    VX  
.unreq    UNIT_X  
.unreq    TMP1  
.unreq    TMP2  
.unreq    DST_W  
.unreq    MASK  
.unreq    SRC_WIDTH_FIXED
```

.else

```
bx    lr /* exit */
```

```
.unreq    SRC  
.unreq    MASK  
.unreq    DST_R  
.unreq    DST_W  
.unreq    W
```

.endif

```
.purgem  fetch_src_pixblock  
.purgem  pixld_src
```

```

        .endfunc
    .endm

    .macro generate_composite_function_single_scanline x:vararg
        generate_composite_function_scanline 0, x
    .endm

    .macro generate_composite_function_nearest_scanline x:vararg
        generate_composite_function_scanline 1, x
    .endm

/* Default prologue/epilogue, nothing special needs to be done */

    .macro default_init
    .endm

    .macro default_cleanup
    .endm

/*
* Prologue/epilogue variant which additionally saves/restores d8-d15
* registers (they need to be saved/restored by callee according to ABI).
* This is required if the code needs to use all the NEON registers.
*/

    .macro default_init_need_all_regs
        vpush    {d8-d15}
    .endm

    .macro default_cleanup_need_all_regs
        vpop     {d8-d15}
    .endm

/*****/

/*
* Conversion of 8 r5g6b6 pixels packed in 128-bit register (in)
* into a planar a8r8g8b8 format (with a, r, g, b color components
* stored into 64-bit registers out_a, out_r, out_g, out_b respectively).
*
* Warning: the conversion is destructive and the original
* value (in) is lost.
*/

    .macro convert_0565_to_8888 in, out_a, out_r, out_g, out_b
        vshrn.u16 out_r, in, #8
        vshrn.u16 out_g, in, #3
        vsli.u16  in, in, #5

```

```

vmov.u8  out_a, #255
vsri.u8  out_r, out_r, #5
vsri.u8  out_g, out_g, #6
vshrn.u16 out_b, in, #2
.endm

.macro convert_0565_to_x888 in, out_r, out_g, out_b
vshrn.u16 out_r, in, #8
vshrn.u16 out_g, in, #3
vsli.u16  in, in, #5
vsri.u8  out_r, out_r, #5
vsri.u8  out_g, out_g, #6
vshrn.u16 out_b, in, #2
.endm

/*
* Conversion from planar a8r8g8b8 format (with a, r, g, b color components
* in 64-bit registers in_a, in_r, in_g, in_b respectively) into 8 r5g6b6
* pixels packed in 128-bit register (out). Requires two temporary 128-bit
* registers (tmp1, tmp2)
*/
.macro convert_8888_to_0565 in_r, in_g, in_b, out, tmp1, tmp2
vshll.u8  tmp1, in_g, #8
vshll.u8  out, in_r, #8
vshll.u8  tmp2, in_b, #8
vsri.u16  out, tmp1, #5
vsri.u16  out, tmp2, #11
.endm

/*
* Conversion of four r5g6b5 pixels (in) to four x8r8g8b8 pixels
* returned in (out0, out1) registers pair. Requires one temporary
* 64-bit register (tmp). 'out1' and 'in' may overlap, the original
* value from 'in' is lost
*/
.macro convert_four_0565_to_x888_packed in, out0, out1, tmp
vshl.u16  out0, in, #5 /* G top 6 bits */
vshl.u16  tmp, in, #11 /* B top 5 bits */
vsri.u16  in, in, #5 /* R is ready in top bits */
vsri.u16  out0, out0, #6 /* G is ready in top bits */
vsri.u16  tmp, tmp, #5 /* B is ready in top bits */
vshr.u16  out1, in, #8 /* R is in place */
vsri.u16  out0, tmp, #8 /* G & B is in place */
vzip.u16  out0, out1 /* everything is in place */
.endm

```

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*/

1.37 libiconv 1.14

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```
<one line to give the program's name and a brief idea of what it does.>  
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```

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```
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```

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1.38 jpeg 9a

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* rdppm.c

*

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*

* This file contains routines to read input images in PPM/PGM format.

* The extended 2-byte-per-sample raw PPM/PGM formats are supported.

* The PBMPLUS library is NOT required to compile this software

* (but it is highly useful as a set of PPM image manipulation programs).

*

* These routines may need modification for non-Unix environments or

* specialized applications. As they stand, they assume input from

* an ordinary stdio stream. They further assume that reading begins

* at the start of the file; start_input may need work if the

* user interface has already read some data (e.g., to determine that

* the file is indeed PPM format).

*/

[...]

/* Portions of this code are based on the PBMPLUS library, which is:

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*/

1.39 daemon-tools 0.76

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1.40 nginx 1.4.7

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1.41 gmp 5.0.5 2012-05-06 Torbjorn Granlund

1.41.1 Available under license :

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Written by: Philip Hazel

Email local part: ph10

Email domain: cam.ac.uk

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```

[zlib]

```
/* zlib.h -- interface of the 'zlib' general purpose compression library
   version 1.2.3, July 18th, 2005
```

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Jean-loup Gailly Mark Adler
jloup@gzip.org madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files <http://www.ietf.org/rfc/rfc1950.txt> (zlib format), [rfc1951.txt](http://www.ietf.org/rfc/rfc1951.txt) (deflate format) and [rfc1952.txt](http://www.ietf.org/rfc/rfc1952.txt) (gzip format).

*/

[edoc, syntax_tools]

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```
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```

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<meta name="generator" content="HTML Tidy, see www.w3.org">
<meta http-equiv="Content-Type" content="text/html; charset=iso-8859-1">
<link rel="stylesheet" type="text/css" href="copyright-software-19980720_files/base.css"><title>W3C IPR
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</body></html>

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"http://www.w3.org/TR/html4/loose.dtd">
<html><head>
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<prepared>Peter Hofmann</prepared>

<docno></docno>

<date>2003-05-26</date>

<rev>A</rev>

<file>licenses.xml</file>

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<p> <marker id="licenses"></marker>

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</p>

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1.44 graphene

f1910b5cdf2969ef97e88125baa7525748eef5d2

1.45 tokumx 2.0.1

1.45.1 Available under license :

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1.46 zope-interface 4.0.5

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1.47 django-tagging 0.3.1 3.e16

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1.50 bundler 1.6.4

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1.51 bundler 1.11.2

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1.52 perl 5.20.0

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*

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```
<one line to give the program's name and a brief idea of what it does.>  
Copyright (C) 19yy <name of author>
```

```
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```

```
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```

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```
Gnomovision version 69, Copyright (C) 19xx name of author
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type 'show w'.
This is free software, and you are welcome to redistribute it
under certain conditions; type 'show c' for details.
```

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```
Yoyodyne, Inc., hereby disclaims all copyright interest in the
program 'Gnomovision' (a program to direct compilers to make passes
at assemblers) written by James Hacker.
```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
```

1.53 gir1.2-gdkpixbuf-2.0 2.27.0

1.53.1 Available under license :

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Version 2.1, February 1999

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```

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1.55 libpng 1.5.22

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```
printf("%s",png_get_copyright(NULL));
```

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Glenn Randers-Pehrson
glennrp at users.sourceforge.net
March 26, 2015

1.56 haproxy 1.7.2

1.56.1 Available under license :

HAPROXY's license - 2006/06/15

Historically, haproxy has been covered by GPL version 2. However, an issue appeared in GPL which will prevent external non-GPL code from being built using the headers provided with haproxy. My long-term goal is to build a core system able to load external modules to support specific application protocols.

Since some protocols are found in rare environments (finance, industry, ...), some of them might be accessible only after signing an NDA. Enforcing GPL on such modules would only prevent them from ever being implemented, while not providing anything useful to ordinary users.

For this reason, I *want* to be able to support binary only external modules when needed, with a GPL core and GPL modules for standard protocols, so that people fixing bugs don't keep them secretly to try to stay over competition.

The solution was then to apply the LGPL license to the exportable include files, while keeping the GPL for all the rest. This way, it still is mandatory to redistribute modified code under customer request, but at the same time, it is expressly permitted to write, compile, link and load non-GPL code using the LGPL header files and not to distribute them if it causes a legal problem.

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Willy Tarreau - w@1wt.eu

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Version 2.1, February 1999

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```
<one line to give the program's name and a brief idea of what it does.>  
Copyright (C) 19yy <name of author>
```

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```
Gnomovision version 69, Copyright (C) 19yy name of author
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it
under certain conditions; type `show c' for details.
```

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```
Yoyodyne, Inc., hereby disclaims all copyright interest in the program
`Gnomovision' (which makes passes at compilers) written by James Hacker.
```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
```

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1.59 log4cxx 0.10.0 13ubuntu2

1.59.1 Available under license :

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Format: <https://www.debian.org/doc/packaging-manuals/copyright-format/1.0/>

Upstream-Name: log4cxx

Source: <https://logging.apache.org/log4cxx/download.html>

Files: *

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Files: debian/*

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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Transitional//EN"

"<http://www.w3.org/TR/xhtml1/DTD/xhtml1-transitional.dtd>">

<!--

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```
--><html xmlns="http://www.w3.org/1999/xhtml">
<head>
  <title>log4cxx - Project License</title>
  <style type="text/css" media="all">
    @import url("../css/maven-base.css");
    @import url("../css/maven-theme.css");
    @import url("../css/site.css");
  </style>
  <link rel="stylesheet" href="../css/print.css" type="text/css" media="print" />
  <meta http-equiv="Content-Type" content="text/html; charset=ISO-8859-1" />
</head>
<body class="composite">
  <div id="banner">
    <a href="http://logging.apache.org/" id="bannerLeft">

      

    </a>
    <div class="clear">
      <hr/>
    </div>
  </div>
  <div id="breadcrumbs">

    <div class="xleft">
      Last Published: 2008-03-31
      |
      <a href="http://www.apache.org/" class="externalLink">Apache</a>
      >

      <a href="http://logging.apache.org/" class="externalLink">Logging Services</a>
      >

      log4cxx
    </div>
  </div>
  <div class="xright">
```

```
</div>
  <div class="clear">
    <hr/>
  </div>
</div>
<div id="leftColumn">
  <div id="navcolumn">
```

```
    <h5>Get log4cxx</h5>
```

```
    <ul>
```

```
    <li class="none">
```

```
      <a href="download.html">Download</a>
```

```
    </li>
```

```
    <li class="none">
```

```
      <a href="changes-report.html">Changes</a>
```

```
    </li>
```

```
    <li class="none">
```

```
      <strong>License</strong>
```

```
    </li>
```

```
    </ul>
```

```
    <h5>About log4cxx</h5>
```

```
    <ul>
```

```
    <li class="none">
```

```
      <a href="index.html">What is log4cxx?</a>
```

```
    </li>
```

```
    <li class="none">
```

```
      <a href="apidocs/index.html">API Documentation</a>
```

```
    </li>
```



```
<li class="none">
  <a href="faq.html">FAQ</a>
</li>
</ul>
  <h5>Community</h5>
  <ul>

<li class="none">
  <a href="mail-lists.html">Mailing Lists</a>
  </li>

<li class="none">
  <a href="issue-tracking.html">Issue Tracking</a>
  </li>

<li class="none">
  <a href="http://wiki.apache.org/logging-log4cxx" class="externalLink">Wiki</a>
  </li>
  </ul>
  <h5>Development</h5>
  <ul>

<li class="none">
  <a href="source-repository.html">Repository</a>
  </li>
```

```
<li class="expanded">
  <a href="">Building</a>
  <ul>
```

```
<li class="none">
  <a href="building/autotools.html">autotools</a>
  </li>
```

```
<li class="none">
  <a href="building/ant.html">Apache Ant</a>
```

```
</li>

<li class="none">
  <a href="building/maven.html">Apache Maven 2</a>
</li>

<li class="none">
  <a href="building/vstudio.html">Microsoft Visual Studio</a>
</li>

<li class="none">
  <a href="building/xcode.html">Apple Xcode</a>
</li>
</ul>
</li>
</ul>
  <h5>Apache</h5>
  <ul>

<li class="none">
  <a href="http://www.apache.org" class="externalLink">Home</a>
</li>

<li class="none">
  <a href="http://www.apache.org/foundation/sponsorship.html" class="externalLink">Sponsorship</a>
</li>

<li class="none">
  <a href="http://www.apache.org/foundation/thanks.html" class="externalLink">Thanks</a>
</li>

<li class="none">
  <a href="http://www.apachecon.com" class="externalLink">Conferences</a>
</li>
</ul>
  <a href="http://maven.apache.org/" title="Built by Maven" class="poweredBy">
  </img>
</a>

</div>
</div>
```

```
<div id="bodyColumn">
  <div id="contentBox">
    <div class="section"><h2>Overview</h2>
    <p>Typically the licenses listed for the project are that of the project itself, and not of dependencies.</p>
  </div>
  <div class="section"><h2>Project License</h2>
  <div class="section"><h3>Apache License, Version 2.0</h3>
  <div class="source"><pre>
      Apache License
      Version 2.0, January 2004
      http://www.apache.org/licenses/
```

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This code is derived from section 17.1 of Applied Cryptography, second edition, which describes a stream cipher allegedly compatible with RSA Labs "RC4" cipher (the actual description of which is a trade secret). The same algorithm is used as a stream cipher called "arcfour" in Tatu Ylonen's ssh package.

Here the stream cipher has been modified always to include the time when initializing the state. That makes it impossible to regenerate the same random sequence twice, so this can't be used for encryption, but will generate good random numbers.

RC4 is a registered trademark of RSA Laboratories.

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This code implements the MD5 message-digest algorithm.
The algorithm is due to Ron Rivest. This code was
written by Colin Plumb in 1993, no copyright is claimed.
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Equivalent code is available from RSA Data Security, Inc.
This code has been tested against that, and is equivalent,
except that you don't need to include two pages of legalese
with every copy.

To compute the message digest of a chunk of bytes, declare an
MD5Context structure, pass it to MD5Init, call MD5Update as
needed on buffers full of bytes, and then call MD5Final, which
will fill a supplied 16-byte array with the digest.

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This code is derived from section 17.1 of Applied Cryptography, second edition, which describes a stream cipher allegedly

compatible with RSA Labs "RC4" cipher (the actual description of which is a trade secret). The same algorithm is used as a stream cipher called "arcfour" in Tatu Ylonen's ssh package.

Here the stream cipher has been modified always to include the time when initializing the state. That makes it impossible to regenerate the same random sequence twice, so this can't be used for encryption, but will generate good random numbers.

RC4 is a registered trademark of RSA Laboratories.

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This code implements the MD5 message-digest algorithm. The algorithm is due to Ron Rivest. This code was written by Colin Plumb in 1993, no copyright is claimed. This code is in the public domain; do with it what you wish.

Equivalent code is available from RSA Data Security, Inc. This code has been tested against that, and is equivalent, except that you don't need to include two pages of legalese with every copy.

To compute the message digest of a chunk of bytes, declare an MD5Context structure, pass it to MD5Init, call MD5Update as needed on buffers full of bytes, and then call MD5Final, which will fill a supplied 16-byte array with the digest.

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Included here to compliment the Pthreads-win32 license header in wtf/ThreadingWin.cpp file. WebKit is using derived sources of ThreadCondition code from Pthreads-win32.

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[See also the ChangeLog file where individuals are attributed in log entries. Likewise in the FAQ file.]

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1.66 openssl 1.0.2j

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